



SMVITM

TECH TARANGA

A DEPARTMENTAL NEWSLETTER
FROM
ELECTRONICS AND
COMMUNICATION ENGINEERING



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HOD's MESSAGE



Greetings to all the readers of *☆☆ Tech Taranga ☆☆*

We cordially invite you all to read this issue of the Electronics and Communication Engineering Department Newsletter. The launch of "*Tech Taranga*" our bi-annual newsletter fills us with excitement. This newsletter will provide a succinct rundown of the department success and activities. We have always used the electronic newsletter *Tech Taranga* to interact with our alumni, professors, students, and industry partners. Additionally, by emphasizing diverse activities including placement, alumni, institutional club activities, student and staff success, it updates the readers on the most recent departmental happenings. We expect more efforts and accomplishments to support the department's continued ascent to excellence. We would like to convey our appreciation to the teaching staff, support staff, and our beloved students for their consistent support.

Dr. Guruprasad

ABOUT DEPARTMENT

Electronics today stands at the forefront of the rapidly expanding horizon of Science and Technology. The Department of Electronics and Communication Engineering in SMVITM was established in the year 2010, initially offering an undergraduate program with an intake of 60 students per year. The intake was increased to 120 in the academic year 2012-13. The department has well-qualified faculty members – highly motivated in teaching and guiding the students in exploring newer avenues of electronics and communication.



The department is intent on creative and technologically advanced skill transfer to the students through teaching, mentoring and counseling. It regularly organizes seminars, symposiums, workshops and invited talks by eminent faculty from reputed institutions and industry experts, to keep the students abreast of the latest technological developments in related fields. The services of some academicians of high repute have been utilized by the department with the objective of supplementing teaching, mentoring and guiding the students as well as faculty members.

The department has its own library comprising of over 200 text books and technical magazines for quick reference. To nurture creative ideas and provide hands-on training to the students, the department has set up an Innovation/Project laboratory with state-of-the-art equipment and latest versions of software tools, in addition to the regular laboratories.

OUR VISION

“To be recognized as a center of eminence in the field of Electronics and Communication Engineering focusing on holistic engineering education and current technologies”.

OUR MISSION

- Impart quality engineering education with ethics to students and transform them into leaders in technology, innovation and research.
- Provide a platform and academic atmosphere that will ensure the transfer of knowledge and skills to the students.
- Promote the overall personality development of the students through activities that have high credibility and societal impact.

PROGRAM EDUCATIONAL OBJECTIVES

The graduate of Electronics & Communication Engineering should be able to:

- Exhibit essential knowledge of applied sciences, mathematical modelling, logical interpretation and virtual realization to resolve real-time problems in the field of Electronics and Communication Engineering.
- Work productively as an Electronics and Communication Engineer, including supportive and leadership roles on multidisciplinary teams.
- Inculcate effective communication skills to excel in professional growth.
- Take part in lifelong learning in pace with the advancing technological society.

PROGRAM OUTCOMES

Graduates of the Electronics and Communication Engineering program are able to:

PO-1	Engineering Knowledge	Develop skills to solve engineering problems by using mathematical, scientific and engineering knowledge.
PO-2	Problem Analysis	Recognize, define, conduct literature survey, examine complex engineering problems and draw conclusions using the principles of mathematics, science and engineering
PO-3	Design/Development of Solutions:	Express ideas, devise implementation strategies, plan execution and synthesize solutions, which are favorable for aspects of public health and safety as well as for cultural, societal and environmental conditions
PO-4	Conduct investigations of complex problems	Investigate complex problems by conducting experiments and validate the results
PO-5	Modern Tool Usage	Employ necessary techniques using modern hardware and software tools for engineering applications
PO-6	The Engineer and Society	Reckon and address the societal, health, safety, legal and cultural issues and adopt responsibilities adhering to professional engineering practice.
PO-7	Environment and Sustainability	Estimate and attend to environmental safety issues by means of engineering practice.
PO-8	Ethics	Understand and apply professional ethics for issues relevant to the engineering practices
PO-9	Individual and Team Work	Work as a member of a multidisciplinary project or research teams and have an understanding of leadership in teams and organizations.
PO-10	Communication	Produce engineering reports and express the ideologies effectively.
PO-11	Project Management and Finance	Apply managerial skill in handling projects as a member and leader of a multi-discipline team
PO-12	Life-long Learning	Evolve through lifelong learning process to keep one updated in technological changes.

PROGRAM SPECIFIC OUTCOMES

Graduates of Electronics & Communication Engineering will be able to:

- Focus on developing and exposure to alternative/ advanced technologies: Understand the concepts of Electronics & Communication Engineering and its application in the fields of signal processing, control systems, embedded systems, VLSI design, networking, and communication.
- Extension of knowledge and testing facilities for the society: Apply domain-specific knowledge to design, analyse, synthesise and validate real-time problems in Electronics & Communication Engineering.

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DEPARTMENTAL ACTIVITIES

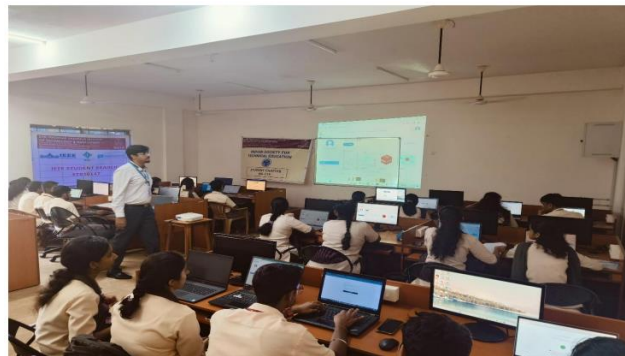
1. One day workshop on "PCB design using open-source tools"

The One Day Workshop on "PCB Design Using Open-Source Tools" was successfully conducted in collaboration with IEEE, ISTE, and IIC at the VLSI Lab, ECE Department, SMVITM, Udupi, on January 16, 2024. The workshop aimed to provide participants with insights into the world of Printed Circuit Board (PCB) design using open-source tools. The workshop, spanning from 9:00 AM to 4:00 PM, was meticulously planned to cover various aspects of PCB design. The schedule included theoretical sessions, hands-on practical exercises, and interactive discussions.

Dr. Shashank M Gowda, a renowned expert in the field and the Director of Invihub Techno solutions, Bengaluru served as the resource person for the workshop. His vast knowledge and practical experience added significant value to the learning experience of the participants and IEEE students. Theoretical sessions provided in-depth insights into the principles and concepts of PCB design using open-source tools. In practical Exercises, participants engaged in hands-on activities, applying the theoretical knowledge to design and simulate PCB layouts under the guidance of Dr. Shashank M Gowda.

Interactive Discussions platform for interactive discussions and Q&A sessions allowed participants to clarify doubts and seek expert advice. The well-equipped facilities provided an ideal environment for both theoretical and practical sessions. The One Day Workshop on "PCB Design Using Open-Source Tools" was a resounding success, thanks to the enthusiastic participation of students and the valuable contribution of Dr. Shashank M Gowda. The event not only enhanced the participant's understanding of PCB design but also served as a platform for learning more skills.

Ms. Ananya K, of 5th semester, student of ECE department, compered the program. The event was coordinated by Ms. Shashikala R, ISTE Coordinator, Ms. Chandana, ISTE Dept coordinator, Mr. Chethan R, ISTE Treasurer and Dr. Sadananda L, IEEE coordinator of the institute.



2. Tech Talk 2.0

The Tech Talk event held on 23rd February 2024 at the Department of Electronics and Communication Engineering at SMVITM, hosted by Ananya K, was a resounding success. The event aimed to provide a platform for students to showcase their knowledge and understanding of various technological topics. The judges for the event were esteemed personalities in the field, Dr. Nagraj Bhat and Dr. Bharti Panjwani. The event commenced with a warm floral welcome to the judges by the ISTE Coordinator, Ms. Shashikala R, Assistant Professor (Senior) in the Department of ECE, and a special acknowledgment of support from the Techno-Crators Club of ECE. Ten Students have participated in this event. Students were given the opportunity to choose from a diverse set of topics for their presentations. The faculty coordinator, Jayashree M, Assistant Professor in the Department of ECE, played a pivotal role in organizing and ensuring the smooth conduct of the event.





The student coordinators, Dhanush Shastry, Pratham Poojary, Ajay Rao and Ananya k all 5th-semester ECE students members of the Techno Crators Club, who contributed to the event's success through their dedicated efforts. Each participant presented their chosen topic with enthusiasm and depth of knowledge. After careful evaluation, the judges declared the winners, acknowledging the hard work and research put forth by the participants. The Techtalk event not only showcased the technical prowess of the students but also fostered an atmosphere of learning, collaboration, and innovation within the college community. The success of this event, with the support of the Techno Crators Club of ECE, sets a promising precedent for future technology-oriented gatherings at SMVITM. At the end of the event Ananya K expressed heartfelt gratitude to all participants for their active participation and the judges in making the TechTalk event a resounding success. The event concluded on a high note as Mr. Arun Upadhyaya, Assistant Professor (Senior) in the Department of ECE, presented letters of appreciation to the esteemed judges, acknowledging their valuable contributions to the insightful discussions.

3. Circuit Debug 2.0

Electronics and Communication Engineering Departments Techno-Crators Club in association with ISTE Students Chapter organized "CIRCUIT DEBUG 2.0" event on 22nd May 2024 at ECE Department. The "Circuit Debug 2.0" event, a stimulating and practical competition aimed at students from various engineering disciplines, was successfully conducted with enthusiastic participation and

expert oversight. The event provided an excellent opportunity for students to apply their theoretical knowledge in a practical setting, fostering a deeper understanding of circuit design and debugging.

The event commenced with a warm welcome by Ananya K, who served as the master of ceremonies. She greeted the participants, volunteers, and distinguished guests, including the esteemed judges Dr. Shilpa Kamath, Associate Professor in the Department of Electronics and Communication and Dr. Rashmi Samanth, Assistant Professor (SG) in the department of AI-ML. The event coordinators, Dhanush Shastry, Pratham Poojary, Ajay Rao, and Ananya K, along with the faculty coordinator, Jayashree M., Assistant Professor in the Department of Electronics and Communication were also acknowledged for their efforts in organizing the event. The competition required participants to form teams of two. This collaborative approach was designed to enhance teamwork and ensure a more interactive learning experience. The event was open to students from the Electronics and Communication Engineering, Artificial and Machine Learning Engineering, and Artificial and Data Science Engineering departments.

The first round tested the participants' foundational knowledge through an aptitude test comprising 25 objective-type questions to be answered within 30 minutes. This round was designed to evaluate the participants' theoretical understanding and problem-solving abilities. Out of all the participating teams, 13 were selected to advance to the next round based on their performance. In the second round, the selected teams were provided with a pre-designed circuit containing deliberate errors. Their task was to identify and correct these errors and then present the corrected circuit to the judges. This round tested their practical skills and their ability to troubleshoot and solve real-world circuit issues. After thorough evaluation

by the judges, 5 teams were chosen to move on to the final round.

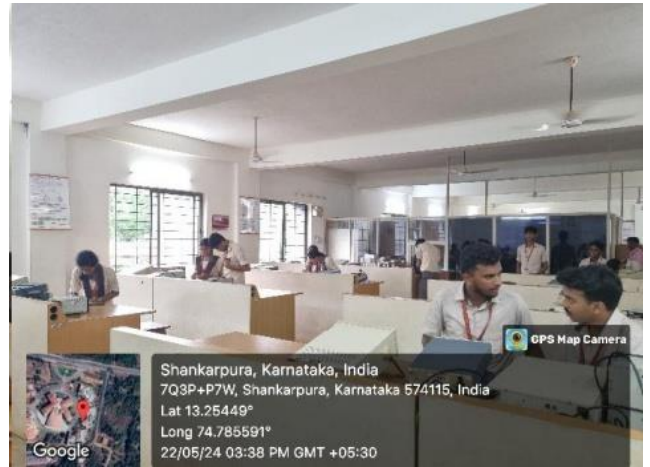


The final round required the teams to assemble the corrected circuit and demonstrate its functionality using a Cathode Ray Oscilloscope (CRO). This round combined theoretical knowledge with practical application, as the participants needed to ensure that their circuit worked correctly and produced the expected output. The ability to successfully rig up the circuit and display the correct output was crucial in this round.



The event concluded with Ananya K. delivering a heartfelt vote of thanks, expressing gratitude to all the participants, volunteers, judges, and coordinators for their enthusiasm and hard work. Dr. Guruprasad, the In-Charge Head of the Department of Electronics and Communication, presented appreciation letters to the judges, acknowledging their valuable contribution to the success of the event. Circuit Debug 2.0 was a highly successful event that provided a valuable learning experience for all involved. It not only enhanced the participants' practical

skills but also fostered teamwork and collaboration. The event's meticulous organization and the active involvement of students and faculty members ensured its success, making it a memorable and enriching experience for everyone.



4. Workshop on PCB Soldering

The Electronics and Communication Engineering Department, in collaboration with the Techno-crators Club cell, organized a workshop on "Advanced Soldering Techniques of Printed Circuit Boards" at the Communication Lab on May 28, 2024. Dr. Guruprasad, Head of the ECE Department, emphasized the significance of PCBs and soldering techniques in contemporary technology. He highlighted the practical applications and the importance of mastering these skills in today's technological landscape. The Master of Ceremonies, Ananya Patkar from 2nd year ECE, efficiently conducted the event and delivered the vote of thanks. Dr. Guruprasad delivered an insightful introduction to the participants, setting the stage for the workshop. Mr. Madhusudan Thanthry P N and Mr. Sandeep K R imparted their expertise on PCB soldering and de-soldering techniques during the workshop sessions. Their hands-on approach and guidance were invaluable to the participants.



The coordination of the entire workshop was skillfully managed by Jayashree M, Assistant Professor, and Pavithra Poornima S, Assistant Professor (Sr.), ensuring a smooth and enriching experience for all attendees.

5. Alumni - Placement Talk "Industry Insights and Trends"

Alumni & Placement Cell from the Dept. of ECE has organized a session on "Industry Insights and Trends". This interaction was held on 27/06/2024, from 2:00pm onwards at B002, Dept. of ECE, SMVITM. The resource person for this interaction was Mr. Ajesh, Assistant Professor, Department of ECE, SMVITM, Bantakal, Alumni of SMVITM. Around 75 students from 6th semester attended the session and interacted with Alumni. The interaction began with a compelling story of his career journey, from their days as a student at SMVITM to current role as Assistant Professor at SMVITM, Bantakal. He briefed students about the current industry trends

and emphasized the importance of continuous learning.



He also shared the importance of taking up NPTEL exam, which is vital for students and professional seeking to enhance their knowledge, skills and employability. Ms Lahari Vaidya, Assistant professor, Dept of ECE hosted the program and the resource person was welcomed by Dr. Guruprasad, Head of the Department of Electronics and Communication Engineering (ECE). Following the technical talk, the students interacted with the resource person. The technical talk concluded with Mr. Arun Upadhyaya, Assistant Professor (SG), extending gratitude to the resource person. Mrs. Yogeshwary B H, Placement coordinator and Ms. Lahari Vaidya, Placement & Alumni Co. convened the program.

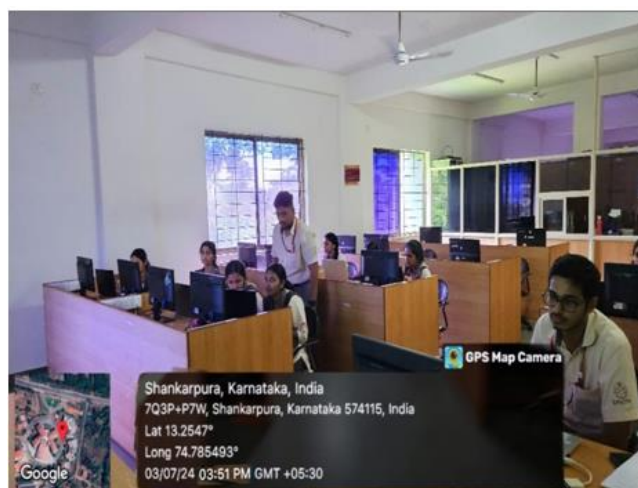
6. Peer learning session on PCB designing using KiCad Software

The Electronics and Communication Engineering Department hosted a peer learning session on PCB design using Ki-Cad software, focusing on advanced design techniques for printed circuit boards. The event took place at the DSP Lab on July 3, 2024.

Dr. Guruprasad, Head of the ECE Department, emphasized the significance of PCB design in today's technology landscape and highlighted its broad applications. He encouraged students to actively engage in such sessions and consider organizing similar events in the future. Ms. Yashaswi, a second-year ECE student, skillfully managed the Master of Ceremonies and delivered the vote of thanks. Dr. Guruprasad also welcomed the participants with an opening address.



From 2nd Year ECE Dept. Mr. Athula A. Bhat provided valuable insights into PCB design, with support from Mr. Adithya R. Das and Mr. Chiranth C. Shet. Ms. Pavithra Poornima S, Senior Assistant Professor coordinated the workshop.



7. Placement Preparation for Engineering Students

Placement Cell of the Department of Electronics & Communication Engineering has organized a talk on "Placement Preparation for Engineering Students", by Ms. Laxmi Shetty, HR Manager, Robo soft Technologies, Udupi on 13 July 2024 from 10AM to 11.30AM at the Seminar Hall, Admin Block, SMVITM.



Ms. Laxmi Shetty explained how the placement preparation is crucial for engineering students aiming to secure employment after graduation and inspired our engineering students prepare effectively for campus placements. She shared key Components of Placement Preparation including Pre-Placement Talks, Aptitude Tests, Technical Tests, Group Discussions, Technical Interviews, HR Interviews. She also spoke about technical and non-technical job opportunities available to students and the skill sets required. She also told that a structured approach to placement preparation, encompassing academic knowledge, technical skills, soft skills, and strategic planning, can significantly enhance the chances of securing desirable positions. Consistent effort and comprehensive preparation are key to success. Around 179 students from 4th & 6th semester ECE participated and interacted with the resource person. Ms. Ananya introduced about Ms. Laxmi Shetty, Dr. Guruprasad, HOD from ECE Dept, was present for the talk. Ms. Yogeshwary B H, and Ms. Lahari Vaidya, department placement coordinators coordinated the activity.

ACCOMPLISHMENTS

8. Faculty Journal Publications

Sl. No.	Name of Authors	Title of paper	Name of Journal
1.	Guruprasad, Chetan R	Comparative Analysis of PMOS and NMOS based Linear Regulators with Similar Power Profiles	Journal of Electrical Science
2.	Nagaraja Rao	Hydro Jet- Efficient Rooftop Cleaning System with External High-Pressure Washer Integration	IRJET
3.	Arun Upadhyaya	XOR Vector Space Based S-Box Generation and Its Application to DES and AES For The Time-Efficient Image Encryption	International Journal on Electrical Engineering and Informatics
4.	Arun Upadhyaya	Chaotic Dynamic S-Box Generation for Image Encryption	International Journal of Recent Scientific Research
5.	Dr. Guruprasad	IoT Smart Electricity Energy Meter	International Journal for Research in Applied Science & Engineering Technology
6.	Chandana	Crop Disease Prediction Using Web Application	International Journal for Research in Applied Science & Engineering Technology
7.	Sowmya Bhat	Wall Painting Robot	International Journal for Research in Applied Science & Engineering Technology
8.	Ganesh Srinivasa Shetty	Strategies for Achieving Energy Efficiency and Data Security Through Data Aggregation in IoT Healthcare Applications: A Comprehensive Study	International Journal of Computer Networks and Applications (IJCNA)
9.	Akshatha Rao L	Hybrid Treadmill Tricycle	International Journal of Creative Research Thoughts
10.	Ganesh Srinivasa Shetty	Design and Implementation of Smart Blind Stick for Obstacle Detection and Navigation System	Journal of Electrical Science
11.	Dr. Shilpa Kamath	Railway Track Crack Detection System	International Journal of Creative Research Thoughts
12.	Ganesh Srinivasa Shetty	Human Voice Recognition for Authentication	IJRASET

9. Faculty Conference Publications

SI.No.	Name of Authors	Title of paper	Name of conference
1.	Chetan R	Precise temperature controlled portable storage module for pharmaceutical fluids	ICFEST 2024
2.	Jayashree M	Real time monitoring of adultration in milk based on machine learning technique in IOT environment	ICFEST 2024
3.	Yogeshwary B H	Wearable cardio respiratory monitoring device for heart attack prediction	ICFEST 2024
4.	Yogeshwary B H	A review of reliability and effective use of energy for underwater sensor network systems	IEEE BHTC 2024

10. Faculty Development Program & Workshops

SI.No.	Name of Faculty	Title of the FDP or Workshop	Venue/College Name
1.	Sowmya Bhat	Fundamental Algorithms: Design and Analysis	NPTEL
2.	Arun Upadhyaya	Recent Industrial Trends in Control & Optimization	Dr. B R Ambedkar Institute of Technology, Jalandhar
3.	Ajesh	Introduction to Internet of Things	NPTEL
4.	Ajesh	AI Evolution: From Foundations to Generative AI	Microsoft/SAP/AICTE/ ATAL
5.	Jayashree M	Integrated Circuits, Mosfets, OP-Amps and their Applications	NPTEL
6.	Yogeshwary B H	Sensors and Actuators	NPTEL
7.	Yogeshwary B H	UTKARSH	SMVITM
8.	Dr. Shilpa Kamath	UTKARSH	SMVITM
9.	Vimitha	UTKARSH	SMVITM
10.	Chandana	UTKARSH	SMVITM
11.	Chetan R	UTKARSH	SMVITM
12.	Ajesh	UTKARSH	SMVITM
13.	Sachin Prabhu K	UTKARSH	SMVITM
14.	Nagaraja Rao	Recent Industrial Trends in control & optimization	Dr. B R Ambedkar Institute of Technology, Jalandhar

11. KSCST/VGST Funded Project Details

Project funding from KSCST

Under the 47th series of Student Project Program (SPP), the evaluation committee had selected the projects submitted by SMVITM students for the sponsorship. Following are the sponsored project lists:

- Mr. Adithya Prakash, Ms. Ashwini, Ms. Athmika Shetty, Mr. Lloyd Winston Pinto, Final year ECE students under the guidance of Ms. Sowmya Bhat and Mr. Lawrence D'Almeida had received the funding for the project titled "Wall Painting Robot".
- Mr. Jeevan Kumar, Ms. Manasa Acharya, Mr. Manish Jogi, Ms. K. Preethika Kamath, Final year ECE students under the guidance of Mr. Chetan R and Ms. Yogeshwary B. H. had received the funding for the project titled "Precise Temperature Controlled Portable Storage Module for Pharmaceutical Fluids".

12. Project Awards

Co-curricular committee in association with ISTE student chapter and IIC of SMVITM, conducted Final Year Project Exhibition on account of National Technology Day in the institute premises. Experts nominated a total of 2 student projects as Best Projects from each department. The I prize was Rs.3,500 and II prize was Rs.2,500 cash was awarded at a farewell function on 11th May 2024. Program was coordinated by cocurricular coordinator Dr. Renita Sharon Monis.

The project titled "Precise Temperature Controlled Portable Storage Module for Pharmaceutical Fluids", has won the best project award under the exhibition category and this project was completed by the final year students Jeevan Kumar, K Preethika Kamath, Manasa Acharya, Manish Jogi from Electronics and Communication Engineering department under the guidance of Mr. Chetan R, Sr. Asst. Professor, ECE Department.



The project titled "Arduino Based Multipurpose Agriculture Robot", has won the runner up under the exhibition category and this project was completed by the final year students

Adithya D Nayak, Ramya Deshapande, Shivaprasad & Shrinidhi Devadiga from Electronics and Communication Engineering department under the guidance of Mr. Raghunatha, Asst. Professor, ECE Department.



13. Co-curricular Achievements by Students

SL No.	Name of the student	Sem	Event	Participati on	Organizer Name/ Location	Date	Prize Won
1.	Dhanush Shastry	5th	Precision Crop Pulse : Agritech	Anveshana 2024	SDM Engineering college Dharwad	13.02.2024 To 14.02.2024	1st
2.	Ajaay Madhav Rao	5th	Precision Crop Pulse : Agritech	Anveshana 2024	SDM Engineering college Dharwad	13.02.2024 To 14.02.2024	1st
3.	K Anusha V Prabhu	3rd	Shrefood.C	Anveshana 2024	SDM Engineering college Dharwad	13.02.2024 To 14.02.2024	Consolation Prize
4.	Dhanush	5th	Line Follower	Incredia 2024	NMAM Institute of Technology	22.02.2024 To 24.02.2024	1st
5.	Sushma						
6.	Nishmitha						
7.	Shreesha	3rd	Sudanva-Arjuna Kalaga	Yakshothsa va 2024	Shri Dharmasthala Manjunatheshw ara Law college	23.02.2024 To 25.02.2024	3rd

					Mangaluru		
8.	Prakhyath P Shetty	1st	Technical Quiz	Tech Yuva 2024	Srinivas Institute of Technology	26.03.2024 To 27.03.2024	1st
9.	Shashank S						
10.	Prasanna Shet	7th	Project EXPO	Tech Yuva 2024	Srinivas Institute of Technology	26.03.2024 To 27.03.2024	1st
11.	Pratham						
12.	Prasanna Shet	7th	Line Follower	Tech Yuva 2024	Srinivas Institute of Technology	26.03.2024 To 27.03.2024	2nd
13.	Pratham						
14.	Prakhyath P Shetty	1st	Electronic Quiz	Saavishkar	Moodlakatte Institute of Technology	01.05.2024 To 02.05.2024	1st
15.	Shashank S						

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING PLACED STARS OF 2024

ABHILASH NAIK	ABHISHEK BHAT	ADITHYA PRAKASH	ANKITHA	ANKITHA A SHET	ANUSHA	ANUSHA
ANUSHA A R	ATHMIKA SHETTY	JEEVAN KUMAR	K PREETHIKA KAMATH	KEERTHANA	LLOYD W PINTO	MANASA ACHARY
MANASA G BHAT	MEDINI S NAIK	NAGARAJ	N G SOWJANYA	NIKHITHA SHETTY	PAVAN SHETTIGAR	PRABHU KUMAR
PRASANNA G SHET	PRASHA	PRATHAM	PRATHVIRAJ	RAHAMATHUNNISA	RAMYA D	RIMSHA
SHREYA M TANTRY	SOUMYA C SHETTY	SOURABH	SUMANTH MUTALIK	TANMAY KALKUR	VIKAS V K	



15. Students Topper List With SGPA

8TH SEMESTER

SI.No.	USN	Name	SGPA
1	4MW20EC012	ANUSHA(RAMACHANDRA)	9.83
2	4MW20EC067	TANMAY KALKUR	9.50
3	4MW20EC035	NIKHITHA SHETTY	9.33
4	4MW20EC010	ANKITHA	9.33

6TH SEMESTER

SI.No.	USN	Name	SGPA
1	4MW21EC013	CHAITANYA ANANT NILEKANI	9.14
2	4MW21EC025	KAVYA (DEVENDRA NAYAK)	9.14
3	4MW21EC020	DISHA	9.00
4	4MW21EC003	ADITHI G RAO	9.00
5	4MW21EC043	PRAJNA PUNYA	9.00

4TH SEMESTER

SI.No.	USN	Name	SGPA
1	4MW22EC006	ANANYA	9.30
2	4MW22EC010	ANNAPURNA SHENOY	9.25
3	4MW22EC053	PRATHAP M	9.20

FACULTIES ACHIEVEMENTS

Dr. Sachin Bhat

- Received "Grass root Innovation Award" 2024 by GoK.
- Authored AICTE text book "Digital Electronics" under NEP in 2024.
- Won Urban Mobility Grand Challenge conducted by Department of IT-BT, GoK in 2023.

Dr.Guruprasad

- Conducted Value added course on Introduction to NGspice.

Dr. Shilpa Kamath

- Conducted Value added course on Introduction to NGspice.

Mr. Arun Upadhyaya

- Reviewer for 2nd IEEE International Conference on Data Science and Information System (ICDSIS-2024) organized by the Department of Information Science and Engineering, Malnad College of Engineering, India in association with IEEE Bangalore Section on 18th -19th May 2024.
- As a Resource Person for the National Innovation and Startup Policy (NISP) and Incubation Opportunities on June 25, 2024, under the Mentor-Mentee Scheme of the Innovation Cell.
- As a Resource Person for the Role of IIC at HEIs and Handling the Innovation Cell Portal on April 15, 2024, under the Mentor-Mentee Scheme of the Innovation Cell.

- Conducted Value added course on Application of MATLAB/SIMULINK in Communication and control engineering.

Mr. Nagaraja Rao

- Conducted Value added course on Application of MATLAB/SIMULINK in Communication and control engineering.

Ms. Shashikala R

- Conducted Value added course on Application of MATLAB/SIMULINK in Communication and control engineering.

Mr. Chetan R

- Conducted Value added course on IOT and Embedded application.
- Resource person for One day session conducted to Atal tinkering lab teachers training held on 20 July 2024 at Little Rock School, Brahmavara.

Ms. Sowmya Bhat

- Conducted Value added course on Introduction to NGspice.

Mr. Ganesh S. Shetty

- Conducted Value added course on Application of MATLAB/SIMULINK in Communication and control engineering.

Mr. Sachin Prabhu

- Conducted Value added course on Programmable Logic Controllers.

Mr. Raghunatha

- Conducted Value added course on Programmable Logic Controllers.

Ms. Yogeshwary B.H

- Reviewed papers in MYSURUCON2024, organized by the Vidya Vikas institute of technology Mysuru on 30th- 31st August 2024.
- Conducted Value added course on Developing applications using MIT app.

Ms. Akshataha Rao

- Conducted Value added course on Developing applications using MIT app.

Ms. Chandana

- Conducted Value added course on Introduction to NGspice.

Ms. Jayashree M

- Conducted Value added course on IOT and Embedded application.

Ms. Poojashree Hebbar

- Conducted Value added course on IOT and Embedded application.

Mr. Ajesh

- Conducted Value added course on Programmable Logic Controllers.

OUT GOING BATCH 2023-24

ECE BATCH 2023-24 (Section -A)



ECE BATCH 2023-24 (Section -B)



CONGRATULATIONS



Mr. Prasanna G Shet
ISTE BEST STUDENT CHAPTER AWARD-2023



Mr. Chetan R
ISTE BEST FACULTY CHAPTER AWARD-2023

CONGRATULATIONS



BEST OUT GOING STUDENT 2023-24
Ms. Ankitha Annappa Shet



GOLD MEDALIST OF ECE 2023-24
Ms. Nikhitha Shetty



SMVITM

**DEPARTMENT OF
ELECTRONICS AND COMMUNICATION ENGINEERING**



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ASST. PROFESSOR**

**MR. CHETAN R
SR. ASST. PROFESSOR**



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