

SHRI MADHWA VADIRAJA INSTITUTE OF TECHNOLOGY AND MANAGEMENT

(A Unit of Shri Sode Vadiraja Mutt Education Trust @, Udupi)
Vishwothama Nagar, BANTAKAL – 574 115, Udupi District, Karnataka, INDIA

Department of Electronics & Communication Engineering



SMVITM

CERTIFICATE

Certified that the Project Work titled 'A Principal Component Analysis Approach for Fault Detection in Analog circuits' is carried out by:

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Bonafide students of Shri Madhwa Vadiraja Institute of Technology and Management, in partial fulfillment for the award of the degree of **Bachelor of Engineering** in Electronics & Communication Engineering of Visvesvaraya Technological University, Belagavi during the year 2022-2023. It is certified that all the corrections / suggestions indicated during Internal Assessment have been incorporated in the report. The report has been approved as it satisfies the academic requirements in respect of Project Work prescribed for the said degree.

Dr. Sachin S Bhat

Professor, Head & Guide
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Dr. Thirumaleshwara Bhat

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Name of the Examiners:

1. Chelain P

2. P. K. Shetty

Signature with Date

22/5/23