



Certificate of Participation

This is to certify that

Chetan R

of

SMVITM, Udupi

has successfully completed the Internship Program on

FPGA Design and Verification

held from 20th October to 19th November 2021, from

Sandeepani School of Embedded System Design, Bangalore.



Ms. Sadiya Arshad

Director Education and Skill Development

www.sandeepani-training.com



Ms. Sadiya
Principal
SRI MATHA YADRAJA
INSTITUTE OF TECHNOLOGY & MANAGEMENT
Vidyanagar, Udupi Nagar, Udupi Dist.
Udupi - 574 115