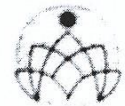




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This is to certify that Dr./Mr./Ms. AVINASH N.J  
from SMVITM, BANTAKAL has participated in  
the Two-day Faculty Development Program on **"VLSI Chip Design &  
Verification using Mentor Graphics EDA Tool"**, held at SMVITM Bantakal,  
organized by the Department of Electronics & Communication Engineering  
in association with **CoreEL Technologies**, Bengaluru and **ISTE Faculty  
Chapter**, on 22 & 23 July 2019.

**Prof. Dr. Balachandra Achar**  
Convener & Head, Dept. of ECE, SMVITM



**Prof. Dr. Thirumaleshwara Bhat**  
Principal, SMVITM, Bantakal

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