| III Sem | | |
|---|---------|--|
| subjects | credits | |
| ENGINEERING MATHEMATICS III | 4 | |
| Analog Electronics | 4 | |
| DIGITAL ELECTRONICS | 4 | |
| Network Analysis | 4 | |
| Electronic Instrumentation | 4 | |
| Engineering Electromagnetics | 4 | |
| Analog Electronics Lab | 2 | |
| Digital Electronics Lab | 2 | |
| IV Sem | | |
| subjects | credits | |
| ENGINEERING MATHEMATICS IV | 4 | |
| Microprocessor | 4 | |
| Control Systems | 4 | |
| Signals and Systems | 4 | |
| Principles of Communication Systems | 4 | |
| Linear Integrated Circuits | 4 | |
| Linear ICs and Communication Lab | 2 | |
| Microprocessor Lab | 2 | |
| V Sem | | |
| subjects | credits | |
| Management and Entrepreneurship Development | 4 | |
| Digital Signal Processing | 4 | |
| Verilog HDL | 4 | |
| Information Theory & Coding | 4 | |
| DSP Lab | 2 | |
| HDL Lab | 2 | |
| Elective subjects | credits | |
| Nanoelectronics | 3 | |
| Switching & Finite Automata Theory | 3 | |
| Operating System | 3 | |
| Electrical Engineering Materials | 3 | |
| MSP430 Microcontroller | 3 | |
| Automotive Electronics | 3 | |
| Object Oriented Programming Using C++ | 3 | |
| 8051 Microcontroller | 3 | |

| VLSI Design Computer Communication Networks Embedded Controller Lab Computer Networks Lab Elective subjects Cellular Mobile Communication 3 Adaptive Signal Processing Artificial Neural Networks Digital Switching Systems 3 Microelectronics Data Structures Using C++ 3 Power Electronics Digital System Design using Verilog VII SEM Subjects Credits Computer Communication Networks 4 Optical Fiber Communication 4 Power Electronics 4 Embedded System Design VLSI LAB 2 POWER ELECTRONICS LAB Elective subjects Credits Applied Embedded System Design 4 Artificial Neural Network 4 DSP Algorithms and Architecture Image Processing Micro and Smart Systems Technology Modeling and Simulation of Data Networks 4 Programming in C plus plus Radio Frequency Integrated Circuits Real Time Systems | VI Sem | |
|--|--|---------|
| ARM Microcontroller & Embedded Systems 4 VLSI Design 4 Computer Communication Networks 4 Embedded Controller Lab 2 Computer Networks Lab 2 Elective subjects credits Cellular Mobile Communication 3 Adaptive Signal Processing 3 Artificial Neural Networks 3 Digital Switching Systems 3 Microelectronics 3 Data Structures Using C++ 3 Power Electronics 3 Digital System Design using Verilog 3 VII SEM Subjects credits Computer Communication Networks 4 Optical Fiber Communication Networks 4 Optical Fiber Communication 4 Power Electronics 4 Embedded System Design 4 VLSI LAB 2 POWER ELECTRONICS LAB 2 Elective subjects credits Applied Embedded System Design 4 Artificial Neural Network 4 CAD For VLSI 4 DSP Algorithms and Architecture 4 Image Processing 4 Micro and Smart Systems Technology 4 Modeling and Simulation of Data Networks 4 Programming in C plus plus 4 Real Time Systems 4 | subjects | credits |
| VLSI Design Computer Communication Networks Embedded Controller Lab Computer Networks Lab Elective subjects Cellular Mobile Communication 3 Adaptive Signal Processing Artificial Neural Networks Digital Switching Systems 3 Microelectronics Data Structures Using C++ 3 Power Electronics Digital System Design using Verilog VII SEM Subjects Credits Computer Communication Networks 4 Optical Fiber Communication 4 Power Electronics 4 Embedded System Design VLSI LAB 2 POWER ELECTRONICS LAB Elective subjects Credits Applied Embedded System Design 4 Artificial Neural Network 4 DSP Algorithms and Architecture Image Processing Micro and Smart Systems Technology Modeling and Simulation of Data Networks 4 Programming in C plus plus Radio Frequency Integrated Circuits Real Time Systems | Digital Communication | 4 |
| Computer Communication Networks Embedded Controller Lab Computer Networks Lab Elective subjects Cellular Mobile Communication Adaptive Signal Processing Artificial Neural Networks Digital Switching Systems 3 Microelectronics 3 Data Structures Using C++ 3 Power Electronics 3 Digital System Design using Verilog 3 VII SEM Subjects Computer Communication Networks 4 Optical Fiber Communication 4 Power Electronics 4 Embedded System Design 4 VLSI LAB POWER ELECTRONICS LAB Elective subjects Credits Applied Embedded System Design 4 Artificial Neural Network Applied Embedded System Design Artificial Neural Network Applied Embedded System Design Applied Embedded System De | ARM Microcontroller & Embedded Systems | 4 |
| Embedded Controller Lab Computer Networks Lab Elective subjects Cellular Mobile Communication Adaptive Signal Processing Artificial Neural Networks Digital Switching Systems 3 Microelectronics Data Structures Using C++ Power Electronics 3 Digital System Design using Verilog 3 VII SEM Subjects Computer Communication Networks 4 Optical Fiber Communication Power Electronics 4 Embedded System Design VLIS LAB POWER ELECTRONICS LAB Elective subjects Credits Applied Embedded System Design 4 Artificial Neural Network 4 CAD for VLSI DSP Algorithms and Architecture 4 Image Processing 4 Micro and Smart Systems Technology 4 Modeling and Simulation of Data Networks 4 Programming in C plus plus Radio Frequency Integrated Circuits 4 Real Time Systems | VLSI Design | 4 |
| Elective subjects credits Cellular Mobile Communication 3 Adaptive Signal Processing 3 Artificial Neural Networks 3 Digital Switching Systems 3 Microelectronics 3 Data Structures Using C++ 3 Power Electronics 3 Digital System Design using Verilog 3 VII SEM Subjects credits Computer Communication Networks 4 Optical Fiber Communication Networks 4 Optical Fiber Communication 4 Power Electronics 4 Embedded System Design 4 VLSI LAB 2 POWER ELECTRONICS LAB 2 Elective subjects credits Applied Embedded System Design 4 Artificial Neural Network 4 CAD for VLSI 4 DSP Algorithms and Architecture 4 Image Processing 4 Micro and Smart Systems Technology 4 Modeling and Simulation of Data Networks 4 Programming in C plus plus 4 Radio Frequency Integrated Circuits 4 Real Time Systems 4 | Computer Communication Networks | 4 |
| Elective subjects Cellular Mobile Communication 3 Adaptive Signal Processing 3 Artificial Neural Networks 3 Digital Switching Systems 3 Microelectronics 3 Data Structures Using C++ 3 Power Electronics 3 Digital System Design using Verilog 3 VII SEM Subjects Computer Communication Networks 4 Optical Fiber Communication 4 Power Electronics 4 Embedded System Design 4 VLSI LAB 2 POWER ELECTRONICS LAB 2 POWER ELECTRONICS LAB 2 Elective subjects Credits Applied Embedded System Design 4 Artificial Neural Network 4 CAD for VLSI DSP Algorithms and Architecture 4 Image Processing Micro and Smart Systems Technology Modeling and Simulation of Data Networks 4 Real Time Systems 4 Real Time Systems | Embedded Controller Lab | 2 |
| Cellular Mobile Communication Adaptive Signal Processing Artificial Neural Networks 3 Digital Switching Systems 3 Microelectronics Data Structures Using C++ Power Electronics 3 Digital System Design using Verilog 3 VII SEM Subjects Credits Computer Communication Networks 4 Optical Fiber Communication 4 Power Electronics 4 Embedded System Design VLSI LAB POWER ELECTRONICS LAB Elective subjects Credits Applied Embedded System Design 4 Artificial Neural Network CAD for VLSI DSP Algorithms and Architecture Image Processing Micro and Smart Systems Technology Modeling and Simulation of Data Networks 4 Real Time Systems 4 Real Time Systems 4 And Sand Sand Simulation of Data Networks 4 Real Time Systems 4 And Sand Sand Simulation of Data Networks 4 Real Time Systems 4 And Sand Sand Simulation of Data Networks 4 Real Time Systems 4 | Computer Networks Lab | 2 |
| Adaptive Signal Processing Artificial Neural Networks Digital Switching Systems Microelectronics Data Structures Using C++ Power Electronics Digital System Design using Verilog 3 VII SEM Subjects Credits Computer Communication Networks 4 Optical Fiber Communication 4 Power Electronics 4 Embedded System Design VLSI LAB POWER ELECTRONICS LAB Elective subjects Credits Applied Embedded System Design 4 Artificial Neural Network Applied Embedded System Design 4 Artificial Neural Network CAD for VLSI DSP Algorithms and Architecture Image Processing Micro and Smart Systems Technology Modeling and Simulation of Data Networks 4 Programming in C plus plus Radio Frequency Integrated Circuits 4 Real Time Systems | Elective subjects | credits |
| Artificial Neural Networks Digital Switching Systems Microelectronics Data Structures Using C++ Power Electronics Digital System Design using Verilog 3 VII SEM Subjects Computer Communication Networks 4 Optical Fiber Communication 4 Power Electronics 4 Embedded System Design VLSI LAB POWER ELECTRONICS LAB Elective subjects Credits Applied Embedded System Design 4 Artificial Neural Network 4 CAD for VLSI DSP Algorithms and Architecture Image Processing Micro and Smart Systems Technology Modeling and Simulation of Data Networks 4 Real Time Systems 4 Real Time Systems 4 Artificigal Systems 4 Artificial Neural Network 4 And Artificial Neural Network 4 Artificial Neu | Cellular Mobile Communication | 3 |
| Digital Switching Systems Microelectronics Data Structures Using C++ Power Electronics Jigital System Design using Verilog Subjects Credits Computer Communication Networks Optical Fiber Communication Power Electronics Embedded System Design VLSI LAB POWER ELECTRONICS LAB Elective subjects Credits Applied Embedded System Design 4 Artificial Neural Network CAD for VLSI DSP Algorithms and Architecture Image Processing Micro and Smart Systems Technology Modeling and Simulation of Data Networks Programming in C plus plus Radio Frequency Integrated Circuits 4 Real Time Systems | Adaptive Signal Processing | 3 |
| Microelectronics Data Structures Using C++ Power Electronics Digital System Design using Verilog Subjects Credits Computer Communication Networks Optical Fiber Communication Power Electronics Embedded System Design VLSI LAB POWER ELECTRONICS LAB Elective subjects Artificial Neural Network CAD for VLSI DSP Algorithms and Architecture Image Processing Micro and Smart Systems Technology Modeling and Simulation of Data Networks Programming in C plus plus Radio Frequency Integrated Circuits A power Electronics 4 CAD for VLSI And CAD for VLSI An | Artificial Neural Networks | 3 |
| Data Structures Using C++ Power Electronics Digital System Design using Verilog 3 VII SEM Subjects Credits Computer Communication Networks 4 Optical Fiber Communication Power Electronics Embedded System Design VLSI LAB POWER ELECTRONICS LAB Elective subjects Credits Applied Embedded System Design 4 Artificial Neural Network CAD for VLSI DSP Algorithms and Architecture Image Processing Micro and Smart Systems Technology Modeling and Simulation of Data Networks Programming in C plus plus Radio Frequency Integrated Circuits Real Time Systems | Digital Switching Systems | 3 |
| Power Electronics 3 Digital System Design using Verilog 3 VII SEM Subjects Credits Computer Communication Networks 4 Optical Fiber Communication | Microelectronics | 3 |
| Digital System Design using Verilog Subjects Credits Computer Communication Networks 4 Optical Fiber Communication Power Electronics Embedded System Design VLSI LAB POWER ELECTRONICS LAB Elective subjects Credits Applied Embedded System Design 4 Artificial Neural Network CAD for VLSI DSP Algorithms and Architecture Image Processing Micro and Smart Systems Technology Modeling and Simulation of Data Networks Programming in C plus plus Radio Frequency Integrated Circuits 4 Credits 4 And And And And And And And | Data Structures Using C++ | 3 |
| VII SEMsubjectscreditsComputer Communication Networks4Optical Fiber Communication4Power Electronics4Embedded System Design4VLSI LAB2POWER ELECTRONICS LAB2Elective subjectscreditsApplied Embedded System Design4Artificial Neural Network4CAD for VLSI4DSP Algorithms and Architecture4Image Processing4Micro and Smart Systems Technology4Modeling and Simulation of Data Networks4Programming in C plus plus4Radio Frequency Integrated Circuits4Real Time Systems4 | Power Electronics | 3 |
| subjectscreditsComputer Communication Networks4Optical Fiber Communication4Power Electronics4Embedded System Design4VLSI LAB2POWER ELECTRONICS LAB2Elective subjectscreditsApplied Embedded System Design4Artificial Neural Network4CAD for VLSI4DSP Algorithms and Architecture4Image Processing4Micro and Smart Systems Technology4Modeling and Simulation of Data Networks4Programming in C plus plus4Radio Frequency Integrated Circuits4Real Time Systems4 | Digital System Design using Verilog | 3 |
| Computer Communication Networks 4 Optical Fiber Communication 4 Power Electronics 4 Embedded System Design 4 VLSI LAB 2 POWER ELECTRONICS LAB 2 Elective subjects credits Applied Embedded System Design 4 Artificial Neural Network 4 CAD for VLSI 4 DSP Algorithms and Architecture 4 Image Processing 4 Micro and Smart Systems Technology 4 Modeling and Simulation of Data Networks 4 Programming in C plus plus 4 Real Time Systems 4 Real Time Systems 4 | VII SEM | |
| Optical Fiber Communication 4 Power Electronics 4 Embedded System Design 4 VLSI LAB 2 POWER ELECTRONICS LAB 2 Flective subjects credits Applied Embedded System Design 4 Artificial Neural Network 4 CAD for VLSI 4 DSP Algorithms and Architecture 4 Image Processing 4 Micro and Smart Systems Technology 4 Modeling and Simulation of Data Networks 4 Programming in C plus plus 4 Radio Frequency Integrated Circuits 4 Real Time Systems 4 | subjects | credits |
| Power Electronics 4 Embedded System Design 4 VLSI LAB 2 POWER ELECTRONICS LAB 2 Elective subjects credits Applied Embedded System Design 4 Artificial Neural Network 4 CAD for VLSI 4 DSP Algorithms and Architecture 4 Image Processing 4 Micro and Smart Systems Technology 4 Modeling and Simulation of Data Networks 4 Programming in C plus plus 4 Radio Frequency Integrated Circuits 4 Real Time Systems 4 | Computer Communication Networks | 4 |
| Embedded System Design 4 VLSI LAB 2 POWER ELECTRONICS LAB 2 Elective subjects credits Applied Embedded System Design 4 Artificial Neural Network 4 CAD for VLSI 4 DSP Algorithms and Architecture 4 Image Processing 4 Micro and Smart Systems Technology 4 Modeling and Simulation of Data Networks 4 Programming in C plus plus 4 Radio Frequency Integrated Circuits 4 Real Time Systems 4 | Optical Fiber Communication | 4 |
| VLSI LAB POWER ELECTRONICS LAB Elective subjects Credits Applied Embedded System Design Artificial Neural Network CAD for VLSI DSP Algorithms and Architecture Image Processing Micro and Smart Systems Technology Modeling and Simulation of Data Networks 4 Programming in C plus plus Radio Frequency Integrated Circuits 4 Real Time Systems 4 2 2 2 2 Credits 4 4 4 4 4 4 4 4 7 7 8 7 8 7 8 7 8 7 8 7 8 7 8 7 8 7 8 7 8 7 8 7 8 7 8 7 8 7 8 7 8 7 8 7 8 8 | Power Electronics | 4 |
| Elective subjects Credits Applied Embedded System Design Artificial Neural Network CAD for VLSI DSP Algorithms and Architecture Image Processing Micro and Smart Systems Technology Modeling and Simulation of Data Networks Programming in C plus plus Radio Frequency Integrated Circuits 4 Credits 4 Credits 4 4 A 4 CAD for VLSI 4 A A A Ball Time Systems 4 CAD for VLSI 4 A A A A A A A A A A A A | Embedded System Design | 4 |
| Elective subjectsApplied Embedded System Design4Artificial Neural Network4CAD for VLSI4DSP Algorithms and Architecture4Image Processing4Micro and Smart Systems Technology4Modeling and Simulation of Data Networks4Programming in C plus plus4Radio Frequency Integrated Circuits4Real Time Systems4 | VLSI LAB | 2 |
| Applied Embedded System Design 4 Artificial Neural Network 4 CAD for VLSI 4 DSP Algorithms and Architecture 4 Image Processing 4 Micro and Smart Systems Technology 4 Modeling and Simulation of Data Networks 4 Programming in C plus plus 4 Radio Frequency Integrated Circuits 4 Real Time Systems 4 | POWER ELECTRONICS LAB | 2 |
| Artificial Neural Network CAD for VLSI DSP Algorithms and Architecture Image Processing Micro and Smart Systems Technology Modeling and Simulation of Data Networks Programming in C plus plus Radio Frequency Integrated Circuits 4 Real Time Systems | Elective subjects | credits |
| CAD for VLSI DSP Algorithms and Architecture Image Processing Micro and Smart Systems Technology Modeling and Simulation of Data Networks 4 Programming in C plus plus Radio Frequency Integrated Circuits 4 Real Time Systems | Applied Embedded System Design | 4 |
| DSP Algorithms and Architecture 4 Image Processing 4 Micro and Smart Systems Technology 4 Modeling and Simulation of Data Networks 4 Programming in C plus plus 4 Radio Frequency Integrated Circuits 4 Real Time Systems 4 | Artificial Neural Network | 4 |
| Image Processing 4 Micro and Smart Systems Technology 4 Modeling and Simulation of Data Networks 4 Programming in C plus plus 4 Radio Frequency Integrated Circuits 4 Real Time Systems 4 | CAD for VLSI | 4 |
| Micro and Smart Systems Technology Modeling and Simulation of Data Networks 4 Programming in C plus plus 4 Radio Frequency Integrated Circuits 4 Real Time Systems | DSP Algorithms and Architecture | 4 |
| Modeling and Simulation of Data Networks 4 Programming in C plus plus 4 Radio Frequency Integrated Circuits 4 Real Time Systems 4 | Image Processing | 4 |
| Programming in C plus plus 4 Radio Frequency Integrated Circuits 4 Real Time Systems 4 | Micro and Smart Systems Technology | 4 |
| Radio Frequency Integrated Circuits 4 Real Time Systems 4 | Modeling and Simulation of Data Networks | 4 |
| Real Time Systems 4 | Programming in C plus plus | 4 |
| | Radio Frequency Integrated Circuits | 4 |
| Speech Processing 4 | Real Time Systems | 4 |
| | Speech Processing | 4 |

| Wavelet Transforms | 4 | |
|-------------------------------------|---------|--|
| VIII Sem | | |
| subjects | credits | |
| Wireless Communication | 4 | |
| Digital Switching Systems | 4 | |
| Project Work | 2 | |
| Seminar | 2 | |
| Elective subjects | credits | |
| Adhoc Wireless Networks | 4 | |
| Distributed Systems | 4 | |
| GSM | 4 | |
| High Performance Computing Networks | 4 | |
| Internet Engineering | 4 | |
| Multimedia Communication | 4 | |
| Network Security | 4 | |
| Optical Computing | 4 | |
| Optical Networks | 4 | |
| Real Time Operating Systems | 4 | |