

Publications:

- **Sowmya Bhat** and Taranath H.B, Design of Low Power and Area Efficient Processor Core, National Conference on Emerging trends in Communication and Bio-medical engineering (NCECB-2014).
- **Sowmya Bhat**, Design of Area Efficient ARM7 Processor Core Using VHDL, 2nd National Conference On Communication And Image Processing – 2014 (NCCIP-14).