

TECH TARANGA

**A Newsletter from the Department of Electronics
and Communication Engineering**



VOL 11

ISSUE 1

**AUG 2025 -
JAN 2026**

HOD's MESSAGE



Greetings to all the readers of



We are delighted to invite you all to explore this issue of the Electronics and Communication Engineering Department Newsletter. The launch of "*TechTaranga*", our bi-annual publication, fills us with excitement as it continues to serve as a platform for sharing the department's achievements and activities.

"TechTaranga" has always been our bridge of communication with alumni, faculty, students, and industry partners. This edition provides insights into various departmental milestones, including placements, alumni engagements, institutional club activities, and the accomplishments of both students and faculty.

As we strive for excellence, we look forward to more innovations and achievements that will further elevate our department. We extend our heartfelt gratitude to our dedicated faculty, support staff, and enthusiastic students for their unwavering contributions and support.

Mr. Arun Upadhyaya

ABOUT DEPARTMENT

Electronics today stands at the forefront of the rapidly expanding horizon of Science and Technology. The Department of Electronics and Communication Engineering in SMVITM was established in the year 2010, initially offering an undergraduate program with an intake of 60 students per year. The intake was increased to 120 in the academic year 2012-13. The department has well-qualified faculty members – highly motivated in teaching and guiding the students in exploring newer avenues of electronics and communication.



The department is intent on creative and technologically advanced skill transfer to the students through teaching, mentoring and counseling. It regularly organizes seminars, symposiums, workshops and invited talks by eminent faculty from reputed institutions and industry experts, to keep the students abreast of the latest technological developments in related fields. The services of some academicians of high repute have been utilized by the department with the objective of supplementing teaching, mentoring and guiding the students as well as faculty members.

The department has its own library comprising of over 200 text books and technical magazines for quick reference. To nurture creative ideas and provide hands-on training to the students, the department has set up an Innovation/Project laboratory with state-of-the-art equipment and latest versions of software tools, in addition to the regular laboratories.

OUR VISION

“To be recognized as a center of eminence in the field of Electronics and Communication Engineering focusing on holistic engineering education and current technologies”.

OUR MISSION

- Impart quality engineering education with ethics to students and transform them into leaders in technology, innovation and research.
- Provide a platform and academic atmosphere that will ensure the transfer of knowledge and skills to the students.
- Promote the overall personality development of the students through activities that have high credibility and societal impact.

PROGRAM EDUCATIONAL OBJECTIVES

The graduate of Electronics & Communication Engineering should be able to:

- Exhibit essential knowledge of applied sciences, mathematical modelling, logical interpretation and virtual realization to resolve real-time problems in the field of Electronics and Communication Engineering.
- Work productively as an Electronics and Communication Engineer, including supportive and leadership roles on multidisciplinary teams.
- Inculcate effective communication skills to excel in professional growth.
- Take part in lifelong learning in pace with the advancing technological society.

PROGRAM SPECIFIC OUTCOMES

Graduates of Electronics & Communication Engineering will be able to:

- Focus on developing and exposure to alternative/ advanced technologies: Understand the concepts of Electronics & Communication Engineering and its application in the fields of signal processing, control systems, embedded systems, VLSI design, networking, and communication.

- Extension of knowledge and testing facilities for the society: Apply domain-specific knowledge to design, analyse, synthesise and validate real-time problems in Electronics & Communication Engineering.

PROGRAM OUTCOMES

Graduates of the Electronics and Communication Engineering program are able to:

PO-1	Engineering Knowledge	Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization for the solution of complex engineering problems.
PO-2	Problem analysis	Identify, formulate, research literature, and analyse complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
PO-3	Design/Development of Solutions	Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for public health and safety, and cultural, societal, and environmental considerations.
PO-4	Conduct investigations of complex problems	Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
PO-5	Engineering Tool Usage	Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modelling to complete engineering activities with a understanding of their limitations.
PO-6	The Engineer and The World	Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal,

		and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
PO-7	Ethics	Apply ethical principles and commit to professional ethics and responsibilities as a norm of the engineering practice.
PO-8	Individual and Collaborative Team work	Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.
PO-9	Communication	Communicate effectively on complex engineering activities with the engineering community and with the society at large, such as being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
PO-10	Project management and finance	Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
PO-11	Life-long learning	Recognise the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

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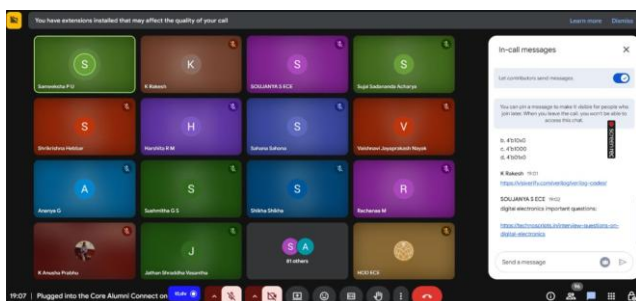
DEPARTMENT ACTIVITIES

1. Online Session on Placement Preparation Skills

The Department of Electronics and Communication Engineering organized an online session on Placement Preparation on 18 August 2025 for fourth-year ECE students. The session aimed to equip students with the essential skills and strategies required to succeed in campus recruitment processes.

The program was hosted by Mr. Arun Upadhyaya, Head of the Department, ECE, and coordinated by Mr. Ganesh UG, Alumni and Placement Coordinator. The session was conducted virtually via Google Meet. Distinguished alumni Ms. Sukhitha, Ms. Sameeksha, Mr. Hemanth, Mr. Rakesh, and Ms. Sowjanya served as the resource persons and shared valuable insights on placement procedures, interview techniques, resume building, and aptitude preparation.

The session concluded with an interactive Q&A, enabling students to clarify their doubts and receive personalized guidance. The program was well received and effectively motivated students by providing a clear roadmap for their placement preparation.



2. Talk on “Core Verilog Concepts: The language of Digital Hardware”

The Department of Electronics and Communication Engineering, in association with the ISTE Student Chapter and the Cocurricular Cell, organized a technical talk on “Verilog and Its Applications in

Digital Design” for fourth-year ECE students on 20 August 2025 at the Seminar Hall, Admin Block. The session was delivered by Mr. Hariprasada, Anmaya Technologies, Udupi.

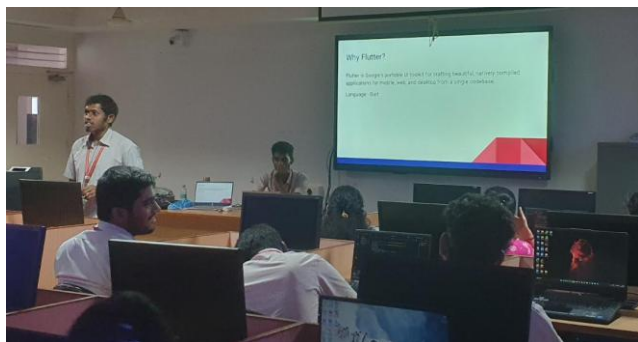
The talk provided insights into the fundamentals of Verilog, its importance in digital system design, and its comparison with other programming languages. Key topics included the design of Half Adder and Full Adder circuits, Design Under Test (DUT), modelling styles in Verilog, and the role of conditional constructs and clocking statements in hardware design. The session was highly informative and enhanced students’ practical understanding of Verilog.

The event was coordinated by Ms. Chandana, ISTE Department Coordinator, with support from Mr. Sandeepa Prabhu, Cocurricular Cell Coordinator. The program concluded with a vote of thanks and the presentation of a token of appreciation to the resource person.



3. Peer learning session on “Github and Flutter for Application Development”

The Department of Electronics and Communication Engineering, in association with the Technocrats Club and IEEE ComSoc, successfully organized a one-day workshop on “GitHub and Flutter for Application Development” on 29 August 2025 at Shri Madhwa Vadiraja Institute of Technology and Management (SMVITM), Bantakal. The workshop was conducted by Mr. Shashank and Mr. Kaushik, students of 5th semester ECE, with the support of Mr. Arun Upadhyaya, Head of the Department, and Ms. Pavithra Poornima S, Assistant Professor (Sr.) and Coordinator, Technocrats Club.



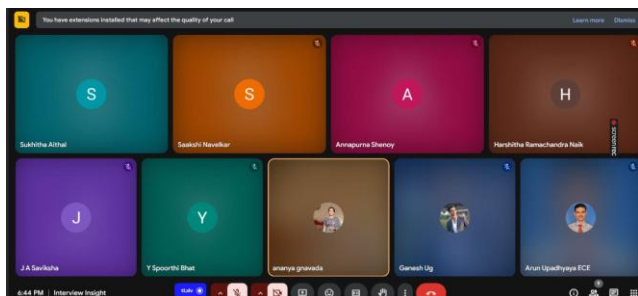
The workshop provided hands-on exposure to Git and GitHub, focusing on version control, repository management, and project tracking. The second session introduced Flutter application development, covering environment setup, Dart basics, and widget-based app creation. The program enhanced students' practical skills in modern software development tools and cross-platform application development.

4. Interview Insight program

The Department of Electronics and Communication Engineering organized a two-day "Interview Insight" program on 29 and 30 August 2025 for students shortlisted for the Mirafra interview process. The program aimed to provide focused interview preparation and personalized guidance to enhance students' confidence and performance.

The session was hosted by Mr. Arun Upadhyaya, Head of the Department, ECE, and coordinated by Mr. Ganesh UG, Alumni and Placement Coordinator. The program was conducted online through Google Meet. Ms. Sukhitha, an esteemed alumna of the department, served as the resource person and conducted one-on-one mock interviews for all 31 shortlisted candidates, offering detailed feedback on technical knowledge, behavioral skills, and interview etiquette.

The initiative was highly effective and well received by the participants, who appreciated the personalized mentoring. The program significantly strengthened the students' readiness for their final interviews and added value to the department's placement preparation activities.



5. Technical Talk on "6G: The Next Frontier in Wireless Communication"

The Department of Electronics and Communication Engineering, in collaboration with the IEEE Communications Society (ComSoc) and ISTE Student Chapter, successfully organized a technical talk for VII Semester ECE students on 30 August 2025. The session was held in the Seminar Hall, Admin Block, with Dr. Prabu K., Associate Professor, Department of ECE, NITK Surathkal, as the resource person.



The program commenced with a floral welcome extended to Dr. Prabu K. by Mr. Arun Upadhyaya, Head of the Department of Electronics and Communication Engineering. The talk opened with an insightful perspective on communication, drawing parallels with elements of nature to emphasize its interconnected and evolving nature. Dr. Prabu K. then presented a brief yet impactful overview of the history of wireless communication, laying a strong foundation for understanding emerging technologies. The session further focused on the concept of 6G technology, highlighting its potential to transform connectivity through innovations such as movable base stations using drones. He elaborated on the vision of 6G as a fully integrated network enabling seamless communication across land, water, and air. The talk also offered valuable insights into the cutting-edge research activities at NITK Surathkal in the domain of next-generation wireless communication.

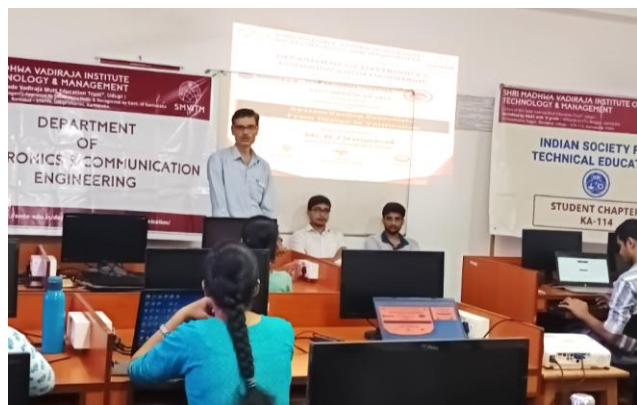
6. Workshop on "System Verilog Essentials: From Design to Verification"

The Department of Electronics and Communication Engineering, in collaboration with the ISTE Student Chapter and the Co-Curricular Cell, successfully organized a three-day workshop on System Verilog

for fourth-year ECE students from 08 to 10 September 2025 in the VLSI Laboratory. Mr. N. J. Hariprasada from Anamaya Technologies, Udupi, along with Mr. Sathwik and Mr. Harish Bhat, served as the resource persons. The program began with a welcome address by Mr. Arun Upadhyaya, Head of the Department, Electronics and Communication Engineering.

The workshop provided participants with both theoretical knowledge and hands-on experience in System Verilog and FPGA-based design. Topics covered included System Verilog data types, Finite State Machines (Mealy and Moore models), FPGA architecture and design flow using Xilinx Vivado, and UART implementation. Hands-on sessions enabled students to design, simulate, and implement hardware modules while developing testbenches and analyzing waveforms. Dr. Shilpa Kamath presented letters of appreciation to the resource persons, and the event was effectively coordinated by Ms. Chandana with support from Mr. Sandeepa Prabhu.

Mr. Ganesh emphasized the importance of setting career goals, strengthening both soft and technical skills, and actively participating in placement training. He encouraged students to learn programming languages like Verilog, use Xilinx software, and seek guidance from seniors for interview preparation. The event was coordinated by Ms. Lahari Vaidya and Ms. Vimitha A. Mr. Arun Upadhyaya, HoD (In-charge), presented a memento to the speaker.



7. Stepping into ECE: Branch Entry 2025

Final-year ECE students organized a vibrant and memorable Branch Entry celebration to warmly welcome the new batch of ECE students on 18 September 2025 at Seminar Hall 3, Library Block. The event aimed to foster a sense of camaraderie and belonging among students while showcasing the spirit and talent of the ECE family. The program commenced with a serene prayer by Ms. Prajna Nayak (fourth year), followed by formal proceedings compered by Ms. Vaishnavi (fourth year ECE). The

event was inaugurated by Dr. Shilpa Kamath, Associate Professor, Department of ECE, and coordinated by Ms. Yogeshwary B. H., Senior Assistant Professor, Department of ECE. All faculty members of the ECE department were present for the occasion. Mr. Chetan R., Assistant Professor (SG), introduced the department, followed by an address on values and academic journey by Mr. Sachin Prabhu, Assistant Professor (SG). A senior talk was delivered by Ms. Gowri P. Shetty.



The celebration featured a variety of icebreaker games such as *Balloon Blast*, *Balloon Cup Pyramid*, and *Things & Run*, providing an engaging platform for interaction between freshers and seniors. These sessions were informally compered by Mr. Shreesha, Ms. Gowri P. Shetty, and Ms. Y. U. Yashaswi, with active support from fourth-year volunteers, and winners were awarded exciting prizes. The event also showcased impressive student talents, including a solo Bharatanatyam performance by Mr. Adithya V. Acharya, a flute recital by Mr. Shreesha Bhat, and energetic group performances by juniors. The entertainment segment concluded with captivating performances by seniors, a group song by Supritha and team, and a dynamic group dance by Team Electro-Infinity, making the celebration lively and memorable.

8. Alumni Connect – Gateway to Opportunities

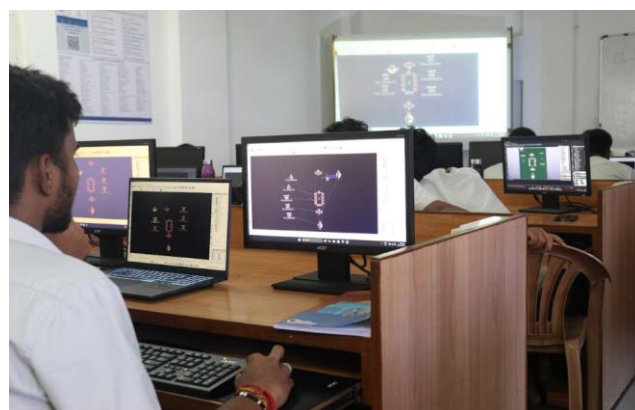
The Department of Electronics and Communication Engineering at Shri Madhwa Vadiraja Institute of Technology and Management (SMVITM), in collaboration with the Placement Cell and Alumni Cell, organized an interactive session titled “Alumni Connect – Gateway to Opportunities” on Saturday, 04 October 2025, at 12:00 PM in the Seminar Hall, Admin Block. The session was delivered by Mr. Dhanush Kumar, an alumnus of SMVITM and currently working at KARMIC Design Pvt. Ltd., Manipal, who shared his professional journey from campus life to the corporate world and offered valuable insights on placement preparation.



Mr. Dhanush Kumar emphasized the importance of technical competence, soft skills, time management, and effective utilization of college resources for career growth. The session concluded with an engaging question-and-answer interaction, where students actively clarified their doubts and gained a deeper understanding of industry expectations. The session motivated students to set clear career goals, continuously enhance their skills, and effectively utilize placement opportunities, while also strengthening alumni–student relationships and fostering a culture of guidance and networking within the department.

9. Peer learning session on KiCad

A one-day workshop on “Peer Learning Session on KiCad” was successfully organized by the Department of Electronics and Communication Engineering, in association with the Technocrats Club and ISTE Student Chapter, at Shri Madhwa Vadiraja Institute of Technology and Management (SMVITM), Bantakal, on 08 October 2025. The session was conducted by Mr. Adithya R. Das, Mr. Athula A. Bhat, Mr. Balaram Bharadwaja Kana, and Mr. Chiranth C. Shet, seventh-semester ECE students, under the guidance of Mr. Arun Upadhyaya, Head of the Department of ECE, and Ms. Pavithra Poornima S., Assistant Professor (Sr.) and Coordinator of the Technocrats Club. The peer learning session aimed to provide hands-on exposure to electronic circuit design and PCB layout development using KiCad, an open-source EDA tool. The workshop covered schematic design, PCB routing, footprint assignment, DRC checks, Gerber file generation, and 3D visualization. Participants actively interacted during the practical sessions and gained a strong foundational understanding of KiCad, motivating them to apply these skills in academic and personal electronics design projects.



10. Logic To Layout: VLSI Design Essentials Workshop

A two-day hands-on workshop titled “Logic to Layout – VLSI Design Essentials” was organized by the Department of Electronics and Communication Engineering, in association with IEEE ComSoc and the Co-Curricular Cell, on 30 and 31 October 2025. The workshop commenced on 30 October 2025 with a formal inauguration in the presence of the Head of the Department and the invited resource persons Mr. Sujay Kumar and Mr. Sandesh Kumar. The inaugural session was compered by Ms. Poornitha, and the resource persons were welcomed with floral gestures by Mr. Sachin Prabhu K. and Mr. Sandeepa Prabhu, Department Co-Curricular Cell Coordinator. The sessions were further supported by trainers Mr. Vijeth Rai and Mr. Sanketh C. Naik, who assisted participants throughout the workshop.



The technical sessions provided extensive hands-on exposure using the Cadence design tool, beginning with PMOS device extraction and layout design, followed by DRC and LVS verification. Participants also designed inverter and level shifter circuits, completing both schematic and layout implementations while learning to identify and

rectify common design errors. The resource persons emphasized essential layout rules, minimum spacing requirements, and practical aspects of VLSI physical design. The workshop concluded with a valedictory session on 31 October, where Dr. Shilpa Kamath S., Department IEEE Coordinator, delivered the closing address and expressed gratitude to the resource persons, trainers, organizers, and participants for making the event a success.

11. Peer learning session- Bots on Track Handson

A one-day workshop titled “Bots on Track” was successfully organized by the Department of Electronics and Communication Engineering, in association with the Technocrats Club and the ISTE Student Chapter On 05 November 2025, at Shri Madhwa Vadiraja Institute of Technology and Management (SMVITM), Bantakal. The workshop was conducted by Mr. Kaushik A., Mr. K. S. Raveesha Padmashali, and Mr. Manvith M. Poojary, fifth-semester ECE students, with the guidance of Mr. Arun Upadhyaya, Head of the Department of ECE, and Ms. Pavithra Poornima S., Assistant Professor (Sr.) and Coordinator of the Technocrats Club. The peer learning session aimed to introduce students to the practical working of line follower robots through a fully hands-on approach. Participants learned how IR sensors detect black and white surfaces, performed circuit connections, assembled the robot chassis, integrated sensors and motors, and implemented programming logic for robot movement.



The session concluded with testing the bots on a marked track and conducting trial runs to evaluate performance. Overall, the workshop provided valuable exposure to robotics concepts, sensor integration, and embedded programming, inspiring students to explore further innovations in robotics.

12. RTL TO GDSII: Explore VLSI using Open-Source Tools

The Department of Electronics and Communication Engineering, in association with IIIC and Anmaya Technologies, organized a one-day hands-on workshop titled “RTL to GDSII: Explore VLSI Using Open-Source Tools” on 08 November 2025 at the DSP Lab, Department of ECE, SMVITM. The offline program was conducted by Mr. N. J. Hariprasad from Anmaya Technologies, Udupi, and was coordinated by Mr. Chetan R. and Ms. Chandana. The workshop was attended by 41 students from V and VII semester ECE, providing them with exposure to the complete VLSI design flow using open-source tools.

The workshop covered the end-to-end VLSI design flow from RTL to GDSII, beginning with basic Linux commands and RTL design of a MUX, including testbench creation and waveform analysis using Icarus Verilog and GTKWave. In the afternoon session, participants designed the schematic and layout of a CMOS inverter using Xschem and Magic, followed by discussions on Static Timing Analysis (STA) using OpenSTA. Students gained hands-on experience with several open-source VLSI tools and developed a clear understanding of how simulation, synthesis, layout, and timing analysis fit into real-world ASIC/FPGA design, making the session both informative and practically enriching.



13. Industry Visit to Karmic Design Pvt. Ltd, Manipal

The Department of Electronics and Communication Engineering organized an industry visit to KARMIC

Design Private Ltd., Manipal, on 11 November 2025, providing students with valuable exposure to the semiconductor industry. The visit effectively bridged theoretical learning with practical experience, enabling students to gain a comprehensive understanding of the VLSI design flow, semiconductor design methodologies, transistor-level design, layout planning, and packaging technologies. Students also understood the importance of precision, matching, and thorough design verification in real-world circuit development.

Furthermore, the visit enhanced students' awareness of industry-relevant skills and professional values essential for success in semiconductor engineering. Interaction with industry professionals and exposure to current design practices strengthened students' motivation to pursue careers in semiconductor design and VLSI technology, thereby reinforcing the importance of industry-academia collaboration.



14. Inauguration of VISTA Club

The Department of Electronics and Communication Engineering inaugurated the VISTA Club (VLSI Innovation in Systems, Technology and Automation) on 19 November 2025 at 11:00 AM in the ECE Department, in collaboration with Anmaya Technology. The inauguration marked a significant milestone for the department, creating new opportunities for innovation, research, and student engagement in the domain of VLSI and advanced electronics. The program began with a warm welcome to the dignitaries, faculty members, staff, and students, highlighting the vision of the club in fostering creativity, technical competence, and collaborative learning.

The ceremony was graced by Dr. Nagaraj Bhat, In-charge Principal; Dr. Sachin Bhat, Dean – R&D; Mr. Arun Upadhyaya, Head of the Department, ECE; Mr. N. J. Hariprasada, CEO of Anmaya Technologies and Chief Guest; and Mr. Chetan R, IIIC Coordinator, along with faculty and students. The inauguration was symbolically marked by switching on the LED display board displaying “VISTA CLUB,” signifying illumination and innovation. The event concluded on an inspiring note, setting the stage for impactful technical activities and industry-oriented learning through the VISTA Club.



15. One Day Workshop on “Basics of PLC Programming”

A hands-on training session on Programmable Logic Controllers (PLCs) was conducted on 21 November 2025, introducing students to Siemens S7 series PLCs, their architecture, and fundamental automation concepts. The session included practical demonstrations using Siemens TIA Portal software, where students learned ladder diagram construction and implemented basic logic gates, latch and unlatch operations, and timer functions such as TP (Pulse Timer), TON (On-Delay Timer), and TOFF (Off-Delay Timer). Real-time automation scenarios were discussed to help students understand the practical application of timers in industrial systems.

The interactive session provided students with exposure to industry-standard tools for PLC simulation and execution, ensuring a strong balance between theory and hands-on programming. As a result, students gained practical experience in PLC programming, improved their understanding of ladder logic and automation workflows, and developed awareness of how automation technologies influence modern industries. The session also

contributed to strengthening students' technical and entrepreneurial competencies in the field of industrial automation.



16. Session on Launch Your Career the Smart Way

The Department of Electronics and Communication Engineering organized a career development session titled “Launch Your Career the Smart Way” on 26 November 2025 for VII Semester ECE students. The session was delivered by Ms. Ashwini M and focused on preparing students for their professional journey through practical guidance on resume building, LinkedIn profile optimization, networking for referrals, selecting valuable certifications, and understanding HR expectations for fresher roles. The interactive nature of the session encouraged active participation and meaningful discussion.

The program helped students gain clarity on effective job search strategies, personal branding, and industry-required skills. As an outcome, students were able to improve their resumes and LinkedIn profiles, identify relevant certifications, and approach upcoming placements with greater confidence and preparedness. The department expressed its gratitude to the resource person for conducting an informative and impactful session.



17. Social Connect & Responsibility – Heritage Village Visit

As part of the Social Connect and Responsibility initiative, 112 second-year students visited Guttu Mane, Heritage Village at Pilikula Nisargadhama on 27 November 2025. The visit aimed to create awareness about local heritage, traditional lifestyles, and the importance of preserving cultural identity. Students explored the traditional Tulunadu ancestral house, observed historical tools and artifacts, and interacted with resource persons who shared valuable insights into cultural practices and rural life.



The visit enhanced students' understanding of heritage conservation and strengthened their sense of social and cultural responsibility. The experiential learning opportunity encouraged students to appreciate traditional knowledge systems and recognize the importance of preserving cultural heritage for future generations.



18. Industry Visit to Schneider Electric India Pvt. Ltd., L&T Technology Services, and Varsha Associates Pvt. Ltd. Mysore

To strengthen industry-academia interaction and inspire innovation, an industry visit was organized for final-year Electronics and Communication Engineering students on 01 December 2025 to Schneider Electric India Pvt. Ltd., L&T Technology Services, and Varsha Associates Pvt. Ltd. The visit

aimed to provide students with real-time exposure to industrial environments and practical engineering applications, while bridging the gap between classroom learning and industry practices in automation, embedded systems, and product engineering. Students also gained insights into modern industrial technologies, workflows, and professional skill expectations.



The industry visit provided substantial practical exposure and enhanced students' understanding of contemporary engineering processes and professional practices. By effectively connecting theoretical concepts with real-world applications, the visit successfully achieved its intended objectives. Overall, the program significantly contributed to improving students' technical awareness, industry readiness, and motivation toward industry-oriented learning, innovation, and career planning.

19. PAAKA SHAALE – Cooking Without Fire

As part of the Social Connect and Responsibility curriculum, the program PAAKA SHAALE – Cooking Without Fire was organized for second-year ECE students on 27 December 2025. The initiative aimed to promote creativity, teamwork, innovation, and awareness of nutritious and eco-friendly food practices, while also fostering social responsibility and essential life skills among students.



About 120 students actively participated by preparing fireless dishes, showcasing originality in food selection, presentation, and healthy ingredient usage. The event was evaluated by Mr. Sarvesh Rao, Assistant Professor, AIDS Department, and Mr. Sandeepa Prabhu, Assistant Professor, ECE Department, based on innovation, hygiene, presentation, nutritional value, and teamwork. The program was successfully coordinated by Mr. Sachin Prabhu and Mr. Ganesh U G, Assistant Professors, ECE Department, and concluded with positive learning outcomes aligned with the course objectives.

20. One day FDP on "RTL TO GDS: Open-Source Design Flow for FPGA and VLSI Systems"

A Faculty Development Programme (FDP) on RTL-to-GDS Open-Source VLSI Design Flow was conducted on 10 January 2026 with the objective of providing hands-on exposure to the complete RTL-to-GDS design process using open-source tools. The programme aimed to familiarize participants with industry-relevant EDA tools for digital design, enhance faculty competency in FPGA and VLSI systems, and promote the adoption of cost-effective open-source solutions in VLSI education and research.



The FDP provided participants with practical exposure to the end-to-end open-source VLSI design flow, enhancing their confidence in using tools such as Icarus Verilog, Yosys, GTKWave, and OpenLane. It increased awareness of industry-standard workflows without reliance on proprietary tools and encouraged faculty to integrate open-source EDA tools into teaching, research, and student projects, thereby strengthening industry-academia interaction in VLSI design.

ACCOMPLISHMENTS

21. Faculty Journal /Book Chapter Publications

Sl. No.	Name of Authors	Title of paper	Name of Journal/Book
1.	Ms. Jayalakshmi K P	Dynamic Spanning Tree Coverage With Q-Learning: A Hybrid Framework for Efficient Robot Navigation in Environments With Moving Obstacles	IEEE Access
2.	Ms. Jayalakshmi K P	MRSimEx-DSTC: A Dynamic Spanning Tree Coverage Approach for Multi-Robot Exploration and Coverage Path Planning	IEEE Access
3.	Ms. Yogeshwary B H	Length dependent hysteresis phenomena in negative capacitance field effect transistor	J. Physics Communication
4.	Mr. Punith Bekal	A systematic review on current and future prospects of 5G communications	Cogent Engineering
5.	Ms. Pavithra Poornima	Synergistic Thinking: Human-Robot Partnership for Smarter Decisions	Wiley Publishers
6.	Ms. Pavithra Poornima	Collaborative Robotics in Factory 5.0: Redefining Modern Production	Wiley Publishers

22. Faculty Conference Publications

Sl. No.	Name of Authors	Title of paper	Name of conference
1.	Sandeepa Prabhu, Yogeshwary B H	Metamaterial Inspired antenna design for applications in diagnosing neurodegenerative abnormalities in infants	Drishti-2025
2.	Chetan R, Jayashree M and Chandana	An IoT based Water Tank Monitoring System Using Node-RED Interactive Dashboards	Drishti-2025
3.	Akshatha Rao L, Shilpa Kamath S	IoT-Enabled Smart Sericulture with Integrated Image Processing	Drishti-2025
4.	Dr. Shilpa Kamath S, Akshatha Rao L	Blood Cancer Classification using Deep Learning on Bone Marrow Cytological Data	Drishti-2025
5.	Chandana	Evaluation and Simulation of an optimized MPPT Technique into solar photovoltaic panels	ICESC 2025
6.	Punith Bekal	Design and Performance Measurement of Microstrip Slot Antenna	INCIP 2025
7.	Punith Bekal	Bandwidth Improvement of Microstrip Antenna Using Defected Ground Structure	INCIP 2025
8.	Ganesh Shetty	Deep Siamese Maxout Forward Harmonic Network (DSMFHN) In IOMT-Based Healthcare Applications	ICOCT), Bangalore
9.	Jayashree M	Design and Implementation of AI-powered Sprayer	Jnana Sangam -2025
10.	Jayalakshmi K P	Decentralized Multi Robot Exploration and Coverage in Dynamic Environments:a framework for Autonomous Cleaning Systems	E2a 2025
11.	Akshatha Rao L	Piezo-Stair: A smart staircase for sustainable energy generation	Engineering Advances 2nd International Conference 2025

23. Faculty Development Program & Workshops

SI. No.	Name of Faculty	Title of the FDP or Workshop	Venue/College Name
1.	Yogeshwary B H	Applied AI: MATLAB based Deep Learning with Hardware Integration	SMVITM, Bantakal
2.	Yogeshwary B H	Smart Teaching and Quality Learning: FDP on OBE Practices and AI in Education	SMVITM, Bantakal
3.	Chetan R	Applied AI: MATLAB based Deep Learning with Hardware Integration	SMVITM, Bantakal
4.	Chetan R	Smart Teaching and Quality Learning: FDP on OBE Practices and AI in Education	SMVITM, Bantakal
5.	Chetan R	RTL to GDS: Open Source Deign Flow for FPGA and VLSI Systems	SMVITM, Bantakal
6.	Sowmya Bhat	Applied AI: MATLAB based Deep Learning with Hardware Integration	SMVITM, Bantakal
7.	Sowmya Bhat	Smart Teaching and Quality Learning: FDP on OBE Practices and AI in Education	SMVITM, Bantakal
8.	Shilpa Kamath	Smart Teaching and Quality Learning: FDP on OBE Practices and AI in Education	SMVITM, Bantakal
9.	Sachin Prabhu	Smart Teaching and Quality Learning: FDP on OBE Practices and AI in Education	SMVITM, Bantakal
10.	Nagaraja Rao	Smart Teaching and Quality Learning: FDP on OBE Practices and AI in Education	SMVITM, Bantakal
11.	Arun Upadhyaya	Smart Teaching and Quality Learning: FDP on OBE Practices and AI in Education	SMVITM, Bantakal
12.	Ganesh U G	RTL to GDS: Open Source Deign Flow for FPGA and VLSI Systems	SMVITM, Bantakal
13.	Ganesh U G	Centre for Engineering Education Excellence (CEEE) Program for Engineering Teachers	IIT, Bombay
14.	Sandeep Prabhu	Centre for Engineering Education Excellence (CEEE) Program for Engineering Teachers	IIT, Bombay
15.	Akshatha Rao L	Smart Teaching and Quality Learning: FDP on OBE Practices and AI in Education	SMVITM, Bantakal
16.	Pavithra Poornima S	Smart Teaching and Quality Learning: FDP on OBE Practices and AI in Education	SMVITM, Bantakal
17.	Ajesh	Applied AI: MATLAB Based Deep Learning with Hardware Integration	SMVITM, Bantakal
18.	Ajesh	Smart Teaching and Quality Learning: FDP on OBE Practices and AI in Education	SMVITM, Bantakal
19.	Akshatha Rao L	Introduction to Machine Learning	IIT, Kharagpur
20.	Raghunatha	Smart Teaching and Quality Learning: FDP on OBE Practices and AI in Education	SMVITM, Bantakal
21.	Jayalakshmi K P	Artificial Intelligence for Research, Innovation and Societal Impact	PVPIT, Maharastra

22.	Jayalakshmi K P	Intelligent Connectivity: AI-Driven Evolution in Next-Gen Communication Networks	SFIT, Mumbai
23.	Jayalakshmi K P	Next-Gen Intelligence: AI, 6G, IoT and Chip Design for Future Systems	NMAMIT, Nitte
24.	Jayashree M	Modelling for Business Decision	SMVITM, Bantakal
25.	Jayashree M	Smart Teaching and Quality Learning: FDP on OBE Practices and AI in Education	SMVITM, Bantakal
26.	Jayashree M	RTL to GDS: Open Source Design Flow for FPGA and VLSI Systems	SMVITM, Bantakal
27.	Sandeepa Prabhu	Smart Teaching and Quality Learning: FDP on OBE Practices and AI in Education	SMVITM, Bantakal
28.	Sandeepa Prabhu	RTL to GDS: Open Source Design Flow for FPGA and VLSI Systems	SMVITM, Bantakal
29.	Sowmya Bhat	Deep Learning	NPTEL
30.	Arun Upadhyaya	Accreditation and Outcome Based Learning	NPTEL
31.	Raghunatha	Accreditation and Outcome Based Learning	NPTEL
32.	Ajesh	Accreditation and Outcome Based Learning	NPTEL
33.	Ajesh	Instruction for Good Learning	NPTEL
34.	Ajesh	Research Methodology	NPTEL
35.	Ajesh	Technical Communication for Engineers	NPTEL
36.	Ajesh	Making Learning Engaging through Interactive Games	NPTEL
37.	Ajesh	Ethics in Engineering Practice	NPTEL
38.	Ajesh	Teaching and Learning in General Programs: TALG	NPTEL
39.	Chandana	Applied AI: MATLAB Based Deep Learning with Hardware Integration	SMVITM, Bantakal
40.	Chandana	Smart Teaching and Quality Learning: FDP on OBE Practices and AI in Education	SMVITM, Bantakal
41.	Jayalakshmi K P	Advanced Tools and Techniques for Research Methodology	RFI, Jabalpur
42.	Jayalakshmi K P	Cyber Security and Privacy	NPTEL
43.	Jayalakshmi K P	RTL to GDS: Open Source Design Flow for FPGA and VLSI Systems	SMVITM, Bantakal
44.	Yogeshwary B H	Innovations in Smart Agriculture: Leveraging Machine Learning, IoT, and Security Frameworks	CEC, Mangalore

45.	Yogeshwary B H	RTL to GDS: Open Source Design Flow for FPGA and VLSI Systems	SMVITM, Bantakal
46.	Ganesh Shetty	Smart Teaching and Quality Learning: FDP on OBE Practices and AI in Education	SMVITM, Bantakal
47.	Ganesh Shetty	Smart Robotics and AI Integration: From Reinforcement Learning to Agentic AI and Geo-Spatial Robotics	NMAMIT, Nitte
48.	Ganesh Shetty	RTL to GDS: Open Source Design Flow for FPGA and VLSI Systems	SMVITM, Bantakal
49.	Punith Bekal	Edunova 360: Rethinking Education, Research and Innovation for Next Decade	SJCE, Chennai
50.	Punith Bekal	Internet of Things (IoT) Design: Devices to Cloud	EPCET, Bengaluru
51.	Shashikala R	Applied AI: MATLAB based Deep Learning with Hardware Integration	SMVITM, Bantakal
52.	Shashikala R	Smart Teaching and Quality Learning: FDP on OBE Practices and AI in Education	SMVITM, Bantakal

24. Co-Curricular Achievements by Students

Sl. No.	Student Name	Event	Date	Place	Stauts
1.	Vilesh Nayak	Technical Symposium & Idea Pitching	23-08-2025	B V Raju Institute of Technology, Narsapur	1st prize
2.	Shashank S Moolya				
3.	Rakshitha R Nayak				
4.	Kaushik A	Line Follower	10-09-2025	Bapuji Institute of Engineering & Technology, Davangere	1st prize
5.	Manvith M Poojary				
6.	Raveesha Padmashali K S				
7.	Shashank S	Dsu Devhack 2.0 National Level Hackathon	12-09-2025 13-09-2025	Dayananda Sagar University, Ramanagara	Participated
8.	Thiru Ganapathy G	Ideathon 2025	19-09-2025	SGBIT, Belagavi	2nd prize
9.	Karthik Sooryanarayan Amin	Line Follower (Techzone Nationals 2025)	11-11-2025	JNNCE Shivamogga	1st prize
10.	Kaushik A				
11.	Raveesha Padmashali K S				
12.	Annapurna Shenoy	Interdisciplinary Conference on Healthcare and Technical Research 2025	14-11-2025	MCHP, Mahe, Manipal	Best Poster Award

13.	Manvith M Poojary	Line Follower	27-09-2025	NITK Surathkal	Participated
		Maze Socker, Line Follower	10-10-2025	MIT Manipal	Participated
		Line Follower	06-11-2025	Sahyadri College Manglore	Participated
14.	Kaushik A	Line Follower	27-09-2025	NITK Surathkal	Participated
		Maze Socker, Line Follower	10-10-2025	MIT Manipal	Participated
		Line Follower, Maze Socker, Roborace, RC Car	10-11-2025 11-11-2025	JNNCE Shivamogga	1st prize
		Line Follower	06-11-2025	Sahyadri College Manglore	Participated
15.	Prasanna	Technical Quiz, Treasure Hunt	12-11-2025	YIT Moodbidri	Participated
16.	Shrilekha	Hardware Hackathon	10-11-2025 11-11-2025	JNNCE Shivamogga	Participated
17.	U Anusha Kumari				
18.	Chirashmi				
19.	Dheeraj Achar	Ideathon 2025	05-12-2025	CIT, Tumkur	Winner
20.	Hrishikesh				

25. Students Topper List With SGPA

7TH Semester

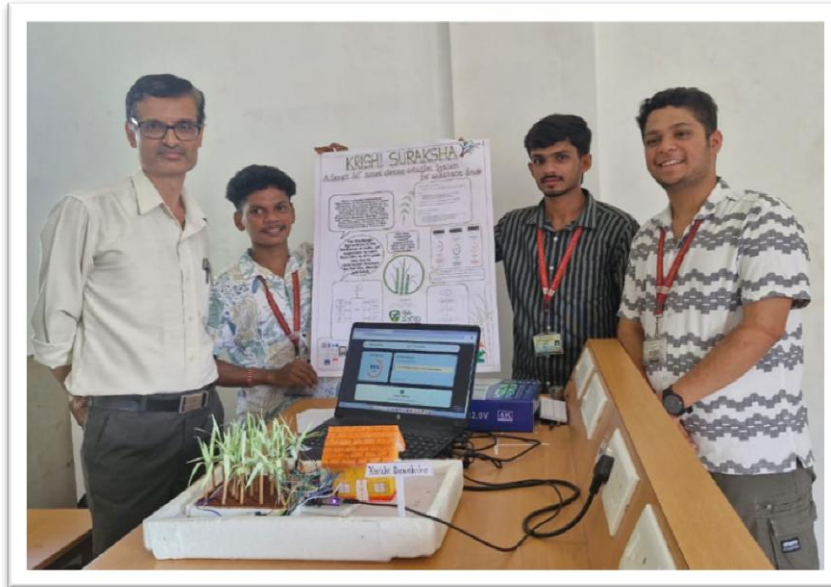
SI. No.	USN	Name	SGPA
1.	4MW22EC010	Annapurna Shenoy	9.25
2.	4MW21EC085	Gowry P Shetty	9.13
3.	4MW22EC034	J A Saviksha	9.08

5TH Semester

SI. No.	USN	Name	SGPA
1.	4MW23EC037	Meghana Rajiv	8.95
2.	4MW23EC083	Sudin Narayana Naik	8.50
3.	4MW23EC044	Pooja	8.27

26. Project Awards

The Co-curricular Committee and Hobby Project Club, in collaboration with the ISTE Student Chapter and IIC of SMVITM, organized the Final Year Project Exhibition on the institute premises. The project titled “Krishi Suraksha A Smart IOT Based Plant Disease Detection System for Sugarcane Crop Fields” won the Best Project Award in the exhibition category. This project was developed by final-year students Mr. Avinash N Karkera, Mr. Chandan N Suvarna, Mr. K Narayan Prabhu, and Mr. Preetham G Acharya from the Electronics and Communication Engineering department, under the guidance of Mr. Arun Upadhyaya, Professor in the ECE Department.



The project titled “Ultrasonic Cleaning and Sorting of Tomatoes Using Computer Vision” secured the runner-up position in the exhibition category. This project was developed by final-year students Ms. Ananya G, Ms. Ananya R Holla, Ms. Harshita R M and Ms. Rakhi from the Electronics and Communication Engineering department, under the guidance of Mr. Raghunatha, Senior Assistant Professor in the ECE Department.



ACHIEVEMENTS AND RECOGNITIONS

Mr. Arun Upadhyaya

- Served as a Reviewer for ICDSNS-2025 on 25–26 June 2025, contributing to the peer-review process of the conference.
- Served as a Journal Reviewer for the *Journal of Basic and Applied Research International* during the academic year 2025–26.
- Successfully completed the NPTEL certification course on “Accreditation and Outcome Based Learning.”
- Served as Session Chair at CISCON-2025, an International Conference held at MIT Manipal on 02 August 2025.
- Served as Resource Person for the workshop “Inspiring Educators – Transforming Futures,” conducted for PU faculty on 28 October 2025.
- Served as Resource Person at Yenapoya University, Mangaluru, delivering a session on “AI Tools for Teachers: Lesson Planning, Content Creation & Classroom Engagement, and AI-Driven Modern Assessment Tools” on 16 December 2025.
- Served as Resource Person at SNM Polytechnic, Moodbidri, delivering a session on “Igniting Innovation: Understanding the Role of IIC” on 08 December 2025.
- Selected as ExeCom Member of the IEEE Mangalore Sub Section, with responsibilities commencing from 01 January 2026.

Mr. Nagaraja Rao

- Recognized as an IEEE Senior Member (2025), reflecting his professional excellence and continued commitment to the advancement of technology.
- Served as Session Chair at CISCON-2025, an International Conference held at MIT Manipal on 02 August 2025.
- Served as a Reviewer for IEEE-NMITCON-2025 during 1–2 August 2025, contributing to the conference review process.
- Selected as ExeCom Member of the IEEE Mangalore Sub Section, with responsibilities commencing from 01 January 2026.

Dr. Shilpa Kamath

- Successfully completed the NPTEL certification course on “Accreditation and Outcome-based Learning.”
- Served as an International Conference Reviewer for ACDSA 2025, held from August 7–9, 2025.
- Served as an International Conference Reviewer for TEMSCON-ASPAC 2025, held from September 14–16, 2025.

- Served as a Judge for Avishkar 2K25 – A Science Fest for Young Minds at SMVITM, Bantakal, on November 15, 2025.
- Served as an International Conference Reviewer for CISCON-2025 at MIT Manipal, held from August 1–2, 2025
- Served as a Reviewer for the 2025 IEEE DISCOVER – IEEE International Conference on Distributed Computing, VLSI, Electrical Circuits and Robotics, held on October 17–18, 2025.
- Served as a Resource Person for a session on “Virtual Tool for Design of Innovative Electronic System” at Government Composite High School, Udupi, on December 24, 2025.

Ms. Shashikala R

- Successfully completed the NPTEL certification course on “Machine Learning and Deep learning, Fundamentals and Application”
- Successfully completed the NPTEL certification course on “Adolescent Health and Well-Being: A Holistic Approach”
- Served as a Reviewer for the International Conference for Women in Computing (INCOWOCO 2025), held on November 14–15, 2025.
- Served as a Reviewer for the 2025 IEEE DISCOVER – IEEE International Conference on Distributed Computing, VLSI, Electrical Circuits and Robotics, held on October 17–18, 2025.
- Served as a Judge for Avishkar 2K25 – A Science Fest for Young Minds at SMVITM, Bantakal, on November 15, 2025.

Mr. Chetan R

- Served as a Judge for TechLikith at SMVITM, a Technical Article Writing Competition, held on 09 September 2025.
- Served as a Resource Person for a One-Day VLSI Workshop held at Mukka on 12 September 2025.
- Served as a Judge for Avishkar 2K25 – A Science Fest for Young Minds at SMVITM, Bantakal, on November 15, 2025.
- Served as a Reviewer for the International Conference for Women in Computing (INCOWOCO 2025), held on November 14–15, 2025.
- Served as a Reviewer for the 1st IEEE International Conference on Application of Information Technologies in Engineering, Management and Science (ICAI-TEMS), held in November 2025.

Ms. Sowmya Bhat

- Successfully completed the NPTEL certification course on “Digital Circuits.”
- Served as a Resource Person for a One-Day VLSI Workshop held at Mukka on 12 September 2025.
- Served as an International Conference Reviewer for TEMSCON-ASPAC 2025, held from September 14–16, 2025.

- Served as a Reviewer for the 2025 IEEE DISCOVER – IEEE International Conference on Distributed Computing, VLSI, Electrical Circuits and Robotics, held on October 17–18, 2025.

Mr. Ganesh S. Shetty

- Served as a Journal Reviewer for Scientific Reports (Q1) in 2025.
- Served as a Journal Reviewer for the Iran Journal of Computer Science (Q2) in 2025.
- Served as a Journal Reviewer for Telecommunication Systems (Q2) in 2025.
- Served as a Journal Reviewer for Cluster Computing (Q1) in 2025.

Mr. Sachin Prabhu

- Successfully completed the NPTEL certification course on “Accreditation and Outcome-based Learning.”
- Served as an International Conference Reviewer for ACDSA 2025, held from August 7–9, 2025.
- Served as a Judge for Avishkar 2K25 – A Science Fest for Young Minds at SMVITM, Bantakal, on November 15, 2025.
- Served as a Presenter at the State-level event held at Azim Premji University, Bengaluru, on October 6, 2025.
- Served as a Reviewer for the 2025 IEEE DISCOVER – IEEE International Conference on Distributed Computing, VLSI, Electrical Circuits and Robotics, held on October 17–18, 2025.
- Served as a Resource Person for a session on “Virtual Tool for Design of Innovative Electronic System” at Government Composite High School, Udupi, on December 24, 2025.

Ms. Jayalakshmi K P

- Successfully completed the NPTEL certification course on “Operating System Fundamentals.”
- Successfully completed the NPTEL certification course on “Cyber Security and Privacy.”
- Served as a Journal Reviewer for the *International Journal of Computational Intelligence Systems* during 4th October 2025.
- Served as a Conference Reviewer for IEEE Discover-2025 during 17–18 October 2025.
- Served as a Journal Reviewer for Discover Vehicles on 08 October 2025.
- Served as a Journal Reviewer for Discover Applied Sciences on 30 October 2025.
- Served as a Conference Reviewer for CISCom 2025 during 26–27 November 2025.
- Awarded the NPTEL Discipline Star accolade in appreciation of outstanding performance during the NPTEL Jul–Dec 2025 semester.

Mr. Raghunatha

- Successfully completed the NPTEL certification course on “Accreditation and Outcome-based Learning.”

- Successfully completed the NPTEL certification course on “Hardware modelling using Verilog.”

Ms. Yogeshwary B.H

- Served as a Reviewer for TEMSMET 2025 during 08–10 October 2025, contributing to the peer-review process.
- Served as a Reviewer for IEEE-DISCOVER during 17–18 October 2025, contributing to the conference review process.
- Served as a Reviewer for TEMSCON-ASPAC 2025 during 14–16 September 2025, contributing to the peer-review process.
- Served as a Resource Person for the session on “Cyber Safety for Smart Students” at Government High School, Indiranagar, Udupi, on November 29, 2025.

Ms. Akshataha Rao

- Successfully completed the NPTEL certification course on “Research Methodology.”
- Successfully completed the NPTEL certification course on “Introduction to Machine Learning.”
- Served as a Reviewer for IEEE COSMIC 2025 – IEEE International Conference on Computing, Semiconductor, Mechatronics, Intelligent Systems, and Communications, held on November 21–22, 2025.
- Served as a Reviewer for the International Conference for Women in Computing (INCOWOCO 2025), held on November 14–15, 2025.
- Served as a Reviewer for the 2025 IEEE DISCOVER – IEEE International Conference on Distributed Computing, VLSI, Electrical Circuits and Robotics, held on October 17–18, 2025.

Ms. Pavithra Poornima

- Successfully completed the NPTEL certification course on “Research Methodology.”
- Served as a Conference Reviewer for IEEE Discover-2025, held on October 17–18, 2025.
- Served as a Conference Reviewer for ICE2CPT 2025, held from October 29 to 31, 2025, during the academic year 2025–26.

Ms. Chandana

- Served as a Reviewer for CISCON-2025, the International Conference held at MIT Manipal on August 1–2, 2025.
- Served as a Reviewer for TEMSCON-ASPAC 2025 during 14–16 September 2025, contributing to the peer-review process.

Ms. Jayashree M

- Served as a Reviewer for the International Conference for Women in Computing (INCOWOCO 2025), held on November 14–15, 2025.
- Successfully completed the NPTEL certification course on “Digital Circuits.”

Ms. Lahari Vaidya

- Served as Resource Person for the one-day workshop “Inspiring Educators – Transforming Futures”, delivering a session on AI-enabled teaching aids, effective counselling techniques, and career guidance for PUC students, organized at SMVITM on 28 October 2025.
- Served as resource Person on “*How to Overcome Adolescence Challenges*” at Shamili PUC, Kidiyoor, Udupi, on 26 September 2025.
- Served as resource Person on “*The Happiness Hack*” during the MBA Orientation Programme 2025–26 at the MBA Block, SMVITM, on 4 November 2025.

Mr. Ajesh

- Successfully completed the NPTEL certification course on “Accreditation and Outcome Based Learning.”, “Instruction for Good Learning.”, “Technical Communication for Engineers.”
- Successfully completed the NPTEL certification course on “Research Methodology.”
- Successfully completed the NPTEL certification course on “Making Learning Engaging through Interactive Games.”
- Served as a Resource Person for a One-Day Workshop on PLC in Industrial Automation at Government Polytechnic, Kaup, on 08 October 2025.
- Served as a Reviewer for the *IEEE-DISCOVER Conference*, held on October 17–18, 2025.
- Served as the Resource Person for a one-day workshop on “PLC in Industrial Automation” at Government Polytechnic, Kaup, on October 8, 2025.
- Successfully completed the SWAYAM–NPTEL Faculty Domain Certification in a single semester, earning the NPTEL Domain Certificate with 82% (575/700), reflecting a strong commitment to continuous professional development and academic excellence.
- Recognized by NPTEL as an NPTEL Enthusiast and NPTEL Believer for the Jul–Dec 2025 session.

Mr. Sandeepa Prabhu

- Successfully completed the NPTEL certification course on “Digital Circuits.”
- Successfully completed the NPTEL certification course on “Digital Logic and Circuit Simulation.”
- Served as an Evaluator for the Internal Smart India Hackathon at SMVITM, Bantakal, on September 19, 2025.
- Served as a Judge for Avishkar 2K25 – A Science Fest for Young Minds at SMVITM, Bantakal, on November 15, 2025.

- Served as a Reviewer for the International Conference for Women in Computing (INCOWOCO 2025), held on November 14–15, 2025.
- Served as a Reviewer for the 2025 IEEE DISCOVER – IEEE International Conference on Distributed Computing, VLSI, Electrical Circuits and Robotics, held on October 17–18, 2025.

Mr. Ganesh U.G.

- Successfully completed the NPTEL certification course on “System Design Through Verilog.”

NEW PARTNERSHIPS & COLLABORATIONS

MoU with Anmaya Technologies, Udupi



The Department of Electronics and Communication Engineering at Shri Madhwa Vadiraja Institute of Technology and Management (SMVITM) signed an MoU with Anmaya Technologies on 14 October 2025 to strengthen academia–industry collaboration in VLSI design, semiconductor technology, and Verilog-based R&D. The partnership will promote hands-on learning, project-based training, industry mentorship, and joint research initiatives. Students will gain exposure to real-time industrial practices through VLSI and semiconductor projects under the department’s VLSI Club, while faculty will engage in knowledge exchange and research activities with technical guidance from the company.

The event was attended by Dr. Radhakrishna S. Aithal, CEO, Sode Group of Institutions, along with Deans, the Head of the Department, ECE faculty, and Mr. N. J. Hariprasad, Founder of Anmaya Technologies.

Glimpses of Achievements



Best Poster Award, MCHP MAHE, Manipal



First place at TECHZONE Nationals 2025



2nd Prize at the VTU Mangaluru Division Table Tennis Tournament 2025–26



First Place at ROBORACE 2025, JNNCE, Shivamogga



First Place @DSU Devhack 2.0

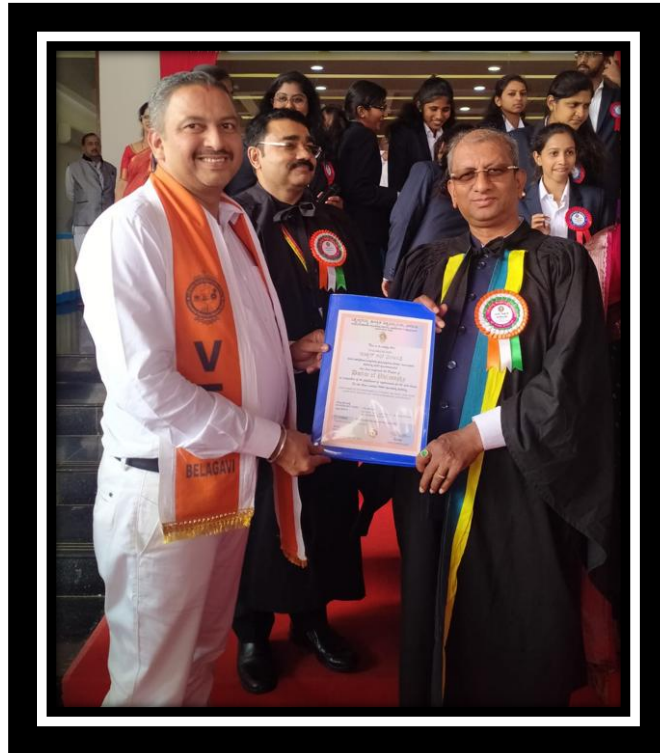


Winner at IDEATHON 2025, SGBIT, Belagavi



First Place at Ideathon 2025, CIT, Tumkur

CONGRATULATIONS



Dr. Nagaraja Rao receiving the Doctoral Degree from VTU, Belagavi.



Dr. Ganesha S. Shetty was awarded the Doctoral Degree from Jain University, Bengaluru.

CONGRATULATIONS TO PLACED STARS OF 2025-26

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**DEPARTMENT OF
ELECTRONICS AND COMMUNICATION ENGINEERING**

PLACED STARS OF 2026 BATCH

 <i>Ananya G</i>	 <i>Annapurna Shenoy</i>	 <i>Anvitha C N</i>	 <i>Balarama B K</i>	 <i>Bhavana B K</i>	
 <i>Bhavana Desai</i>	 <i>Dhruv Patil S N</i>	 <i>Harshitha R Naik</i>	 <i>J A Saviksha</i>	 <i>Karthik Patil</i>	
 <i>Nasreen M</i>	 <i>Prajna Nayak</i>	 <i>Prathap M</i>	 <i>Shreesha</i>	 <i>Vaishnavi</i>	
 <i>Vashnavi</i>	 <i>Y U Yashawi</i>	 <i>Vaishnavi</i>	 <i>Spandana G A</i>	 <i>Shreya R S</i>	 <i>Shrikrishna</i>





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A Newsletter from the Department of Electronics and Communication Engineering



News Letter Editors

Ms. Jayashree M, Asst. Professor

Mr. Adithya R Das, Student Coordinator

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