

ABOUT THE INSTITUTION

Established in the year 2010 under the auspices of Shri Sode Vadiraja Mutt Education Trust ®, Udupi, Shri Madhwa Vadiraja Institute of Technology & Management (SMVITM) has emerged as 'A Promising Institution with a Distinction'. H. H. Shri Vishwavallabha Theertha Swamiji of Shri Sode Vadiraja Mutt, Udupi and the President of the Trust, at a very young age, has promoted SMVITM to provide quality higher education to the needy student community at affordable costs. Though the institution is very young, its accomplishments are commendable. Having admired the vision and value addition to engineering education at the institute, the Former President of India, Bharat Ratna Dr. A P J Abdul Kalam made a prestigious visit to SMVITM campus on 02 April 2014 and interacted with the students on the topic "Empowering the Nation through Technology and Innovation".



ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering in SMVITM was established in the year 2010, initially offering an undergraduate program with an intake of 60 students per year. The intake was increased to 120 in the academic year 2012-13. The department is intent on creative and technologically advanced skill transfer to the students through teaching, mentoring and counseling. It regularly organizes short-term courses, seminars, symposiums, workshops and invited talks by eminent faculty from reputed institutions and industry experts, to keep the students abreast of the latest technological developments in related fields. The services of some academicians of high repute have been utilized by the department with the objective of supplementing teaching, mentoring and guiding the students as well as faculty members.



Three-Day Short Course

In

ANALOG ELECTRONIC DESIGN

8-10 January 2016

By

Prof. P. R. Mukund, RIT, USA

Organized by

ISTE Faculty Chapter



**SHRI MADHWA VADIRAJA
INSTITUTE OF
TECHNOLOGY & MANAGEMENT**



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ABOUT THE COURSE

This course will give a firm foundation in current analog integrated circuit design methodologies. It will focus on circuit concepts and design techniques. No physical design will be done. The material covered will include:

- IC devices, both passive and active
- DC biasing circuits, including constant g_m current sources and bandgap reference circuits.
- Two stage operational trans-conductance amplifier
- Frequency response and stability analysis
- Cascode output stage operational amplifier
- Introduction to mixed-signal design, including comparators, sample and hold circuits, analog to digital converters and phase locked loops

TEXT BOOK

- “Analog Integrated Circuit Design”, 2nd edition, T.C. Carusone, D. Johns & K. Martin, Wiley Student Edition, 2013

Note: It is mandatory for the participants to possess the above text book.



THE RESOURCE PERSON

Prof. P. R. Mukund is a professor at RIT, Rochester, NY in Electrical Engineering. His area of expertise is analog and RF integrated circuit design. He has been the Principal Investigator of research projects funded by the National Science Foundation, Semiconductor Research Corporation, Eastman Kodak, Harris Corp., K-Micro, RAMBUS, Inc., LSI Logic Corp, and others with funding in excess of \$2 million. He was a Guest Editor of IEEE Computer, a Distinguished Lecturer of IEEE, General of IEEE SOCC, and given invited talks in India, Malaysia, Thailand, Argentina, USA, etc. He has published extensively in refereed journals and conferences, and has supervised the graduate research of over 70 students. He is also a Visiting Professor, GC member and the Chairman of External Advisory Board of SMVITM, Bantakal.

TARGET AUDIENCE

“Young” faculty members of engineering colleges with B.E. / M.Tech in Electronics or related field

The participants should be willing to spend about 6-8 contact hours each day during the course, and about 3-4 hours outside class each day.

Familiarity with design tools such as Cadence will be an added advantage.

LAB PROJECT

The participants will design a simple two stage OTA and perform corner analysis.

TESTS

There will be two tests given on the second and third day of the course

DATES & TIMINGS

8 – 10 Jan 2016 (Friday – Sunday)
9:00 am to 5:00 pm on all the 3 days

VENUE

Central Computer Centre (CCP Lab)
Basement Floor, Admin Block, SMVITM

FEES & MODE OF PAYMENT

Rs. 1000 /- on the spot payment

REGISTRATION

Maximum 20 participants are allowed, on first come first serve basis. So, confirm your participation immediately by sending email, or calling over the mobile:

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